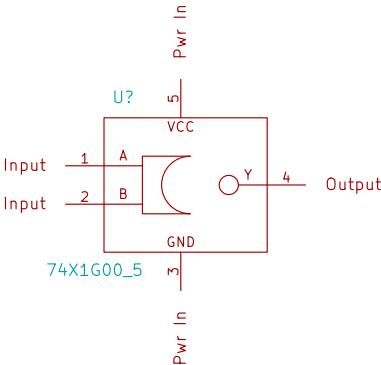


Logic > Single > 74x1g00_5

74X1G00_5

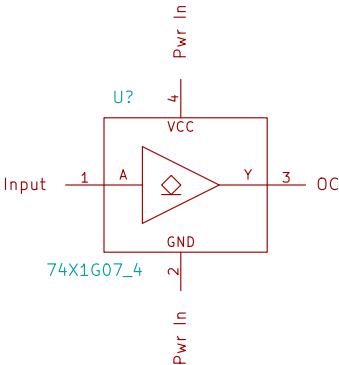
/home/qi/kicad-libs/components/74x1g00_5.lib



Logic > Single > 74x1g07_4

74X1G07_4

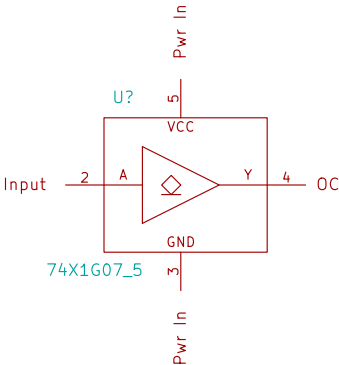
/home/qi/kicad-libs/components/74x1g07_4.lib



Logic > Single > 74x1g07_5

74X1G07_5

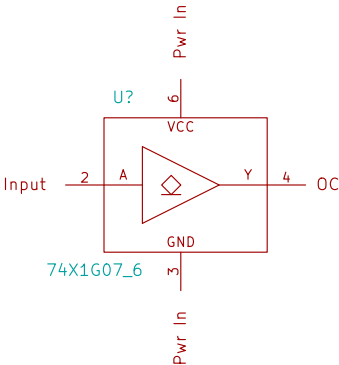
/home/qi/kicad-libs/components/74x1g07_5.lib



Logic > Single > 74x1g07_6

74X1G07_6

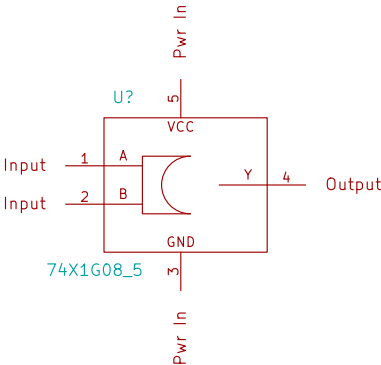
/home/qi/kicad-libs/components/74x1g07_6.lib



Logic > Single > 74x1g08_5

74X1G08_5

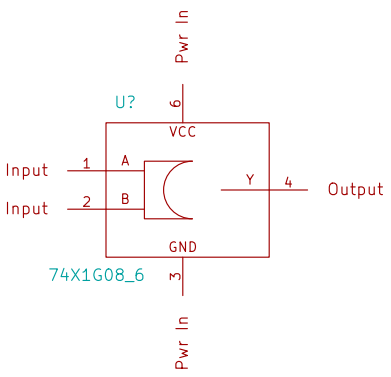
/home/qi/kicad-libs/components/74x1g08_5.lib



Logic > Single > 74x1g08_6

74X1G08_6

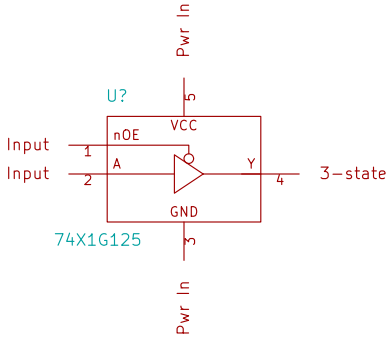
/home/qi/kicad-libs/components/74x1g08_6.lib



Logic > Single > 74x1g125

74X1G125

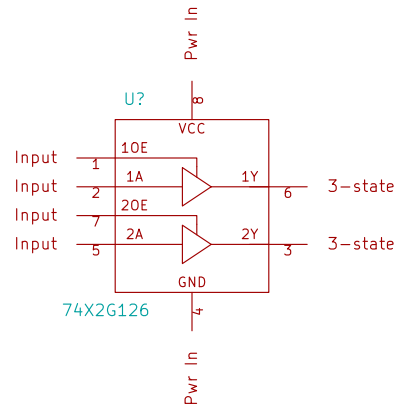
/home/qi/kicad-libs/components/74x1g125.lib



Logic > Dual > 74x2g126

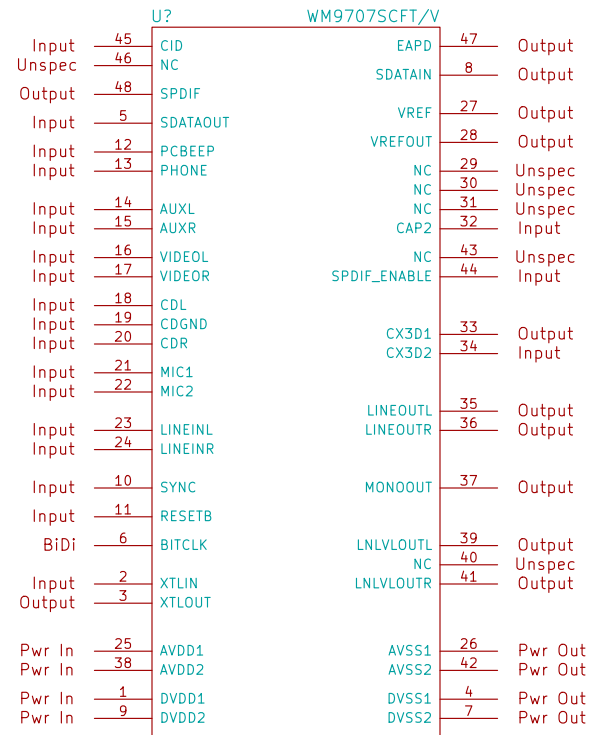
74X2G126

/home/qi/kicad-libs/components/74x2g126.lib



WM9707SCFT/V

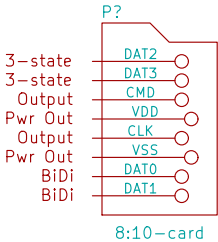
/home/qi/kicad-lib/components/wolfson.lib



Connector > Card > 8:10-card

8:10-card

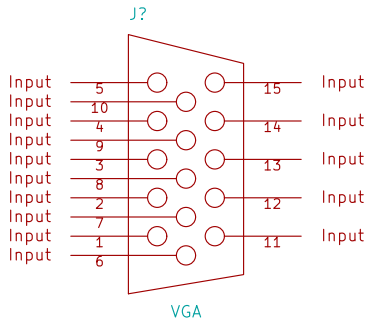
/home/qi/kicad-libs/components/8_10-card.lib



Connector > Video > vga

VGA

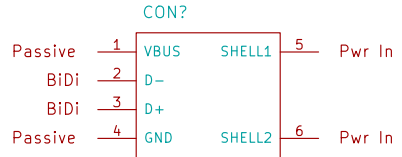
/home/qi/kicad-libs/components/vga.lib



Connector > USB > usb_a_plug

USB_A_PLUG

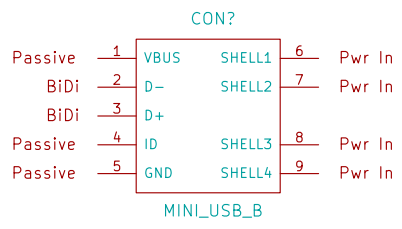
/home/qi/kicad-libs/components/usb_a_plug.lib



Connector > USB > mini_usb_b

MINI_USB_B

/home/qi/kicad-libs/components/mini_usb_b.lib



FPGA > Xilinx > xc6slx45-2fgg484c

XC6SLX45-2FGG484C (A)

/home/qi/kicad-libs/components/xc6slx45-2fgg484c.lib

BiDi	A3	IO_L1P_HSWAPEN_0	IO_L36P_GCLK15_0	D11	BiDi
BiDi	A4	IO_L1N_VREF_0	IO_L36N_GCLK14_0	C12	BiDi
BiDi	C5	IO_L2P_0	IO_L37P_GCLK13_0	B12	BiDi
BiDi	A5	IO_L2N_0	IO_L37N_GCLK12_0	A12	BiDi
BiDi	D6	IO_L3P_0	IO_L38P_0	C13	BiDi
BiDi	C6	IO_L3N_0	IO_L38N_VREF_0	A13	BiDi
BiDi	B6	IO_L4P_0	IO_L49P_0	D14	BiDi
BiDi	A6	IO_L4N_0	IO_L49N_0	C14	BiDi
BiDi	C7	IO_L5P_0	IO_L50P_0	B14	BiDi
BiDi	A7	IO_L5N_0	IO_L50N_0	A14	BiDi
BiDi	B8	IO_L6P_0	IO_L51P_0	C15	BiDi
BiDi	A8	IO_L6N_0	IO_L51N_0	A15	BiDi
BiDi	D9	IO_L7P_0	IO_L62P_0	D15	BiDi
BiDi	C8	IO_L7N_0	IO_L62N_VREF_0	C16	BiDi
BiDi	C9	IO_L8P_0	IO_L63P_SCP7_0	B16	BiDi
BiDi	A9	IO_L8N_VREF_0	IO_L63N_SCP6_0	A16	BiDi
BiDi	D7	IO_L32P_0	IO_L64P_SCP5_0	C17	BiDi
BiDi	D8	IO_L32N_0	IO_L64N_SCP4_0	A17	BiDi
BiDi	D10	IO_L33P_0	IO_L65P_SCP3_0	B18	BiDi
BiDi	C10	IO_L33N_0	IO_L65N_SCP2_0	A18	BiDi
BiDi	B10	IO_L34P_GCLK19_0	IO_L66P_SCP1_0	E16	BiDi
BiDi	A10	IO_L34N_GCLK18_0	IO_L66N_SCP0_0	D17	BiDi
BiDi	C11	IO_L35P_GCLK17_0			
BiDi	A11	IO_L35N_GCLK16_0			
Pwr In	B11	VCC0_0			
Pwr In	B15	VCC0_0			
Pwr In	B19	VCC0_0			
Pwr In	B4	VCC0_0			
Pwr In	B7	VCC0_0			
Pwr In	E13	VCC0_0			
Pwr In	E17	VCC0_0			
Pwr In	E9	VCC0_0			
Pwr In	G10	VCC0_0			
Pwr In	G14	VCC0_0			

FPGA > Xilinx > xc6slx45-2fgg484c

XC6SLX45-2FGG484C (B)

/home/qi/kicad-libs/components/xc6slx45-2fgg484c.lib

BiDi	L19	IO_L42N_GCLK6_TRDY1_M1LDM_1	IO_L1P_A25_1	C19	BiDi
BiDi	J20	IO_L43P_GCLK5_M1DQ4_1	IO_L1N_A24_VREF_1	B20	BiDi
BiDi	J22	IO_L43N_GCLK4_M1DQ5_1	IO_L9P_1	G16	BiDi
BiDi	K21	IO_L44P_A3_M1DQ6_1	IO_L9N_1	G17	BiDi
BiDi	K22	IO_L44N_A2_M1DQ7_1	IO_L10P_1	F16	BiDi
BiDi	L20	IO_L45P_A1_M1DQ5_1	IO_L10N_1	F17	BiDi
BiDi	L22	IO_L45N_A0_M1DQ5N_1	IO_L19P_1	B21	BiDi
BiDi	M21	IO_L46P_FCS_B_M1DQ2_1	IO_L19N_1	B22	BiDi
BiDi	M22	IO_L46N_FOE_B_M1DQ3_1	IO_L20P_1	A20	BiDi
BiDi	N20	IO_L47P_FWE_B_M1DQ0_1	IO_L20N_1	A21	BiDi
BiDi	N22	IO_L47N_LDC_M1DQ1_1	IO_L21P_1	K16	BiDi
BiDi	P22	IO_L48N_M1DQ9_1	IO_L21N_1	J16	BiDi
BiDi	P21	IO_L48P_HDC_M1DQ8_1	IO_L28P_1	H16	BiDi
BiDi	R20	IO_L49P_M1DQ10_1	IO_L28N_VREF_1	H17	BiDi
BiDi	R22	IO_L49N_M1DQ11_1	IO_L29P_A23_M1A13_1	D19	BiDi
BiDi	T21	IO_L50P_M1UDQ5_1	IO_L29N_A22_M1A14_1	D20	BiDi
BiDi	T22	IO_L50N_M1UDQ5N_1	IO_L30P_A21_M1RESET_1	F18	BiDi
BiDi	U20	IO_L51P_M1DQ12_1	IO_L30N_A20_M1A11_1	F19	BiDi
BiDi	U22	IO_L51N_M1DQ13_1	IO_L31P_A19_M1CKE_1	D21	BiDi
BiDi	V21	IO_L52P_M1DQ14_1	IO_L31N_A18_M1A12_1	D22	BiDi
BiDi	V22	IO_L52N_M1DQ15_1	IO_L32P_A17_M1A8_1	C20	BiDi
BiDi	M19	IO_L53P_1	IO_L32N_A16_M1A9_1	C22	BiDi
BiDi	N19	IO_L53N_VREF_1	IO_L33P_A15_M1A10_1	G19	BiDi
BiDi	M16	IO_L58P_1	IO_L33N_A14_M1A4_1	F20	BiDi
BiDi	L15	IO_L58N_1	IO_L34P_A13_M1WE_1	H19	BiDi
BiDi	P19	IO_L59P_1	IO_L34N_A12_M1BA2_1	H18	BiDi
BiDi	P20	IO_L59N_1	IO_L35P_A11_M1A7_1	E20	BiDi
BiDi	W20	IO_L60P_1	IO_L35N_A10_M1A2_1	E22	BiDi
BiDi	W22	IO_L60N_1	IO_L36P_A9_M1BA0_1	J17	BiDi
BiDi	L17	IO_L61P_1	IO_L36N_A8_M1BA1_1	K17	BiDi
BiDi	K18	IO_L61N_1	IO_L37P_A7_M1A0_1	F21	BiDi
BiDi	U19	IO_L70P_1	IO_L37N_A6_M1A1_1	F22	BiDi
BiDi	V20	IO_L70N_1	IO_L38P_A5_M1CLK_1	H20	BiDi
BiDi	M17	IO_L71P_1	IO_L38N_A4_M1CLKN_1	J19	BiDi
BiDi	M18	IO_L71N_1	IO_L39P_M1A3_1	G20	BiDi
BiDi	P17	IO_L72P_1	IO_L39N_M1ODT_1	G22	BiDi
BiDi	N16	IO_L72N_1	IO_L40P_GCLK11_M1A5_1	K20	BiDi
BiDi	P18	IO_L73P_1	IO_L40N_GCLK10_M1A6_1	K19	BiDi
BiDi	R19	IO_L73N_1	IO_L41P_GCLK9_IRDY1_M1RASN_1	H21	BiDi
BiDi	T19	IO_L74P_AWAKE_1	IO_L41N_GCLK8_M1CASN_1	H22	BiDi
BiDi	T20	IO_L74N_DOUT_BUSY_1	IO_L42P_GCLK7_M1UDM_1	M20	BiDi
Pwr In	W21	VCC0_1			
Pwr In	U18	VCC0_1			
Pwr In	R21	VCC0_1			
Pwr In	N18	VCC0_1			
Pwr In	L21	VCC0_1			
Pwr In	L16	VCC0_1			
Pwr In	J18	VCC0_1			
Pwr In	G21	VCC0_1			
Pwr In	E19	VCC0_1			
Pwr In	C21	VCC0_1			

FPGA > Xilinx > xc6slx45-2fgg484c

XC6SLX45-2FGG484C (C)

/home/qi/kicad-libs/components/xc6slx45-2fgg484c.lib

BiDi	<u>Y21</u>	IO_L1P_CCLK_2	IO_L65N_CS0_B_2	<u>T5</u>	BiDi
BiDi	<u>AA22</u>	IO_L1N_M0_CMPMISO_2	IO_L65P_INIT_B_2	<u>T6</u>	BiDi
BiDi	<u>AA21</u>	IO_L2P_CMPCLK_2	IO_L64N_D9_2	<u>AB2</u>	BiDi
BiDi	<u>AB21</u>	IO_L2N_CMPMOSI_2	IO_L64P_D8_2	<u>AA2</u>	BiDi
BiDi	<u>AA20</u>	IO_L3P_D0_DIN_MISO_MISO1_2	IO_L63N_2	<u>V5</u>	BiDi
BiDi	<u>AB20</u>	IO_L3N_MOSI_CSI_B_MISO0_2	IO_L63P_2	<u>U6</u>	BiDi
BiDi	<u>T18</u>	IO_L4P_2	IO_L62N_D6_2	<u>Y4</u>	BiDi
BiDi	<u>T17</u>	IO_L4N_VREF_2	IO_L62P_D5_2	<u>W4</u>	BiDi
BiDi	<u>Y19</u>	IO_L5P_2	IO_L60N_2	<u>R7</u>	BiDi
BiDi	<u>AB19</u>	IO_L5N_2	IO_L60P_2	<u>T7</u>	BiDi
BiDi	<u>W18</u>	IO_L6P_2	IO_L59N_2	<u>R8</u>	BiDi
BiDi	<u>Y18</u>	IO_L6N_2	IO_L59P_2	<u>R9</u>	BiDi
BiDi	<u>T16</u>	IO_L7P_2	IO_L58N_2	<u>AB3</u>	BiDi
BiDi	<u>T15</u>	IO_L7N_2	IO_L58P_2	<u>Y3</u>	BiDi
BiDi	<u>U17</u>	IO_L8P_2	IO_L57N_2	<u>AB4</u>	BiDi
BiDi	<u>U16</u>	IO_L8N_2	IO_L57P_2	<u>AA4</u>	BiDi
BiDi	<u>V19</u>	IO_L9P_2	IO_L54N_2	<u>AB5</u>	BiDi
BiDi	<u>V18</u>	IO_L9N_2	IO_L54P_2	<u>Y5</u>	BiDi
BiDi	<u>R16</u>	IO_L10P_2	IO_L53N_2	<u>Y6</u>	BiDi
BiDi	<u>R15</u>	IO_L10N_2	IO_L53P_2	<u>W6</u>	BiDi
BiDi	<u>V17</u>	IO_L11P_2	IO_L52N_2	<u>U10</u>	BiDi
BiDi	<u>W17</u>	IO_L11N_2	IO_L52P_2	<u>T10</u>	BiDi
BiDi	<u>U14</u>	IO_L12P_D1_MISO2_2	IO_L51N_2	<u>U8</u>	BiDi
BiDi	<u>U13</u>	IO_L12N_D2_MISO3_2	IO_L51P_2	<u>T8</u>	BiDi
BiDi	<u>U15</u>	IO_L13P_M1_2	IO_L50N_2	<u>V9</u>	BiDi
BiDi	<u>V15</u>	IO_L13N_D10_2	IO_L50P_2	<u>U9</u>	BiDi
BiDi	<u>AA18</u>	IO_L14P_D11_2	IO_L49N_D4_2	<u>AB6</u>	BiDi
BiDi	<u>AB18</u>	IO_L14N_D12_2	IO_L49P_D3_2	<u>AA6</u>	BiDi
BiDi	<u>Y17</u>	IO_L15P_2	IO_L49N_RDWR_B_VREF_2	<u>AB7</u>	BiDi
BiDi	<u>AB17</u>	IO_L15N_2	IO_L48P_D7_2	<u>Y7</u>	BiDi
BiDi	<u>AA14</u>	IO_L16P_2	IO_L47N_2	<u>Y8</u>	BiDi
BiDi	<u>AB14</u>	IO_L16N_VREF_2	IO_L47P_2	<u>W9</u>	BiDi
BiDi	<u>Y16</u>	IO_L17P_2	IO_L46N_2	<u>V7</u>	BiDi
BiDi	<u>W15</u>	IO_L17N_2	IO_L46P_2	<u>W8</u>	BiDi
BiDi	<u>V13</u>	IO_L18P_2	IO_L45N_2	<u>AB8</u>	BiDi
BiDi	<u>W13</u>	IO_L18N_2	IO_L45P_2	<u>AA8</u>	BiDi
BiDi	<u>AA16</u>	IO_L19P_2	IO_L44N_2	<u>Y10</u>	BiDi
BiDi	<u>AB16</u>	IO_L19N_2	IO_L44P_2	<u>W10</u>	BiDi
BiDi	<u>W14</u>	IO_L20P_2	IO_L43N_2	<u>AB9</u>	BiDi
BiDi	<u>Y14</u>	IO_L20N_2	IO_L43P_2	<u>Y9</u>	BiDi
BiDi	<u>Y15</u>	IO_L21P_2	IO_L42N_2	<u>W11</u>	BiDi
BiDi	<u>AB15</u>	IO_L21N_2	IO_L42P_2	<u>V11</u>	BiDi
BiDi	<u>T12</u>	IO_L22P_2	IO_L41N_VREF_2	<u>AB10</u>	BiDi
BiDi	<u>U12</u>	IO_L22N_2	IO_L41P_2	<u>AA10</u>	BiDi
BiDi	<u>T14</u>	IO_L23P_2	IO_L40P_2	<u>R11</u>	BiDi
BiDi	<u>R13</u>	IO_L23N_2	IO_L40N_2	<u>T11</u>	BiDi
BiDi	<u>W12</u>	IO_L29P_GCLK3_2	IO_L32N_GCLK28_2	<u>AB11</u>	BiDi
BiDi	<u>Y12</u>	IO_L29N_GCLK2_2	IO_L32P_GCLK29_2	<u>Y11</u>	BiDi
BiDi	<u>Y13</u>	IO_L30P_GCLK1_D13_2	IO_L31N_GCLK30_D15_2	<u>AB12</u>	BiDi
BiDi	<u>AB13</u>	IO_L30N_GCLK0_USERCCLK_2	IO_L31P_GCLK31_D14_2	<u>AA12</u>	BiDi
Pwr In	<u>AA11</u>	VCC0_2			
Pwr In	<u>AA15</u>	VCC0_2			
Pwr In	<u>AA19</u>	VCC0_2			
Pwr In	<u>AA3</u>	VCC0_2			
Pwr In	<u>AA7</u>	VCC0_2			
Pwr In	<u>T13</u>	VCC0_2			
Pwr In	<u>T9</u>	VCC0_2			
Pwr In	<u>V12</u>	VCC0_2			
Pwr In	<u>V16</u>	VCC0_2			
Pwr In	<u>V8</u>	VCC0_2			
Pwr In	<u>W5</u>	VCC0_2			

FPGA > Xilinx > xc6slx45-2fgg484c

XC6SLX45-2FGG484C (D)

/home/qi/kicad-libs/components/xc6slx45-2fgg484c.lib

BiDi	B3	IO_L83N_VREF_3		IO_L1P_3	Y2	BiDi
BiDi	A2	IO_L83P_3		IO_L1N_VREF_3	Y1	BiDi
BiDi	E6	IO_L82N_3		IO_L2P_3	W3	BiDi
BiDi	E5	IO_L82P_3		IO_L2N_3	W1	BiDi
BiDi	C4	IO_L81N_3		IO_L7P_3	P8	BiDi
BiDi	D3	IO_L81P_3		IO_L7N_3	P7	BiDi
BiDi	F7	IO_L80N_3		IO_L8P_3	P6	BiDi
BiDi	G7	IO_L80P_3		IO_L8N_3	P5	BiDi
BiDi	B2	IO_L60P_3		IO_L9P_3	T4	BiDi
BiDi	H8	IO_L59N_3		IO_L9N_3	T3	BiDi
BiDi	J7	IO_L59P_3		IO_L10P_3	U4	BiDi
BiDi	H8	IO_L59N_3		IO_L10N_3	V3	BiDi
BiDi	E4	IO_L58N_3		IO_L11P_3	N6	BiDi
BiDi	D5	IO_L58P_3		IO_L11N_3	N7	BiDi
BiDi	K8	IO_L57N_VREF_3		IO_L23P_3	M7	BiDi
BiDi	K7	IO_L57P_3		IO_L23N_3	M8	BiDi
BiDi	F5	IO_L55N_M3A14_3		IO_L24P_3	R4	BiDi
BiDi	G6	IO_L55P_M3A13_3		IO_L24N_3	P4	BiDi
BiDi	C1	IO_L54N_M3A11_3		IO_L25P_3	M6	BiDi
BiDi	C3	IO_L54P_M3RESET_3		IO_L25N_3	L6	BiDi
BiDi	D1	IO_L53N_M3A12_3		IO_L26P_3	P3	BiDi
BiDi	D2	IO_L53P_M3CKE_3		IO_L26N_3	N4	BiDi
BiDi	E1	IO_L52N_M3A9_3		IO_L31P_3	M5	BiDi
BiDi	E3	IO_L52P_M3A8_3		IO_L31N_VREF_3	M4	BiDi
BiDi	F3	IO_L51N_M3A4_3		IO_L32P_M3DQ14_3	V2	BiDi
BiDi	G4	IO_L51P_M3A10_3		IO_L32N_M3DQ15_3	V1	BiDi
BiDi	F1	IO_L50N_M3BA2_3	U?D	IO_L33P_M3DQ12_3	U3	BiDi
BiDi	F2	IO_L50P_M3WE_3	XC6SLX45-2FGG484C	IO_L33N_M3DQ13_3	U1	BiDi
BiDi	H5	IO_L49N_M3A2_3		IO_L34P_M3UDQS_3	T2	BiDi
BiDi	H6	IO_L49P_M3A7_3		IO_L34N_M3UDQS_3	T1	BiDi
BiDi	G1	IO_L48N_M3BA1_3		IO_L35P_M3DQ10_3	R3	BiDi
BiDi	G3	IO_L48P_M3BA0_3		IO_L35N_M3DQ11_3	R1	BiDi
BiDi	H1	IO_L47N_M3A1_3		IO_L36P_M3DQ8_3	P2	BiDi
BiDi	H2	IO_L47P_M3A0_3		IO_L36N_M3DQ9_3	P1	BiDi
BiDi	H3	IO_L46N_M3CLKN_3		IO_L37P_M3DQ0_3	N3	BiDi
BiDi	H4	IO_L46P_M3CLK_3		IO_L37N_M3DQ1_3	N1	BiDi
BiDi	J6	IO_L45N_M3ODT_3		IO_L38P_M3DQ2_3	M2	BiDi
BiDi	K6	IO_L45P_M3A3_3		IO_L38N_M3DQ3_3	M1	BiDi
BiDi	J4	IO_L44N_GCLK20_M3A6_3		IO_L39P_M3LDQS_3	L3	BiDi
BiDi	K3	IO_L44P_GCLK21_M3A5_3		IO_L39N_M3LDQS_3	L1	BiDi
BiDi	K4	IO_L43N_GCLK22_IRDY2_M3CASN_3		IO_L40P_M3DQ6_3	K2	BiDi
BiDi	K5	IO_L43P_GCLK23_M3RASN_3		IO_L40N_M3DQ7_3	K1	BiDi
BiDi	L4	IO_L42N_GCLK24_M3LDM_3		IO_L41P_GCLK27_M3DQ4_3	J3	BiDi
BiDi	M3	IO_L42P_GCLK25_TRDY2_M3UDM_3		IO_L41N_GCLK26_M3DQ5_3	J1	BiDi
Pwr In	F4	VCC0_3				
Pwr In	C2	VCC0_3				
Pwr In	W2	VCC0_3				
Pwr In	U5	VCC0_3				
Pwr In	R2	VCC0_3				
Pwr In	N5	VCC0_3				
Pwr In	L7	VCC0_3				
Pwr In	L2	VCC0_3				
Pwr In	J5	VCC0_3				
Pwr In	G2	VCC0_3				
Pwr In	F6	VCC0_3				

FPGA > Xilinx > xc6slx45-2fgg484c

XC6SLX45-2FGG484C (E)

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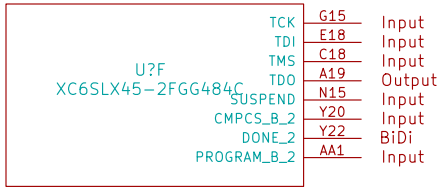
Pwr In	J21	GND	VCCAUX	D16	Pwr In
Pwr In	J9	GND	VCCAUX	F11	Pwr In
Pwr In	K10	GND	VCCAUX	G12	Pwr In
Pwr In	K12	GND	VCCAUX	H15	Pwr In
Pwr In	K14	GND	VCCAUX	H9	Pwr In
Pwr In	L11	GND	VCCAUX	K15	Pwr In
Pwr In	L13	GND	VCCAUX	L8	Pwr In
Pwr In	L18	GND	VCCAUX	M15	Pwr In
Pwr In	L5	GND	VCCAUX	N8	Pwr In
Pwr In	L9	GND	VCCAUX	R10	Pwr In
Pwr In	M10	GND	VCCAUX	R12	Pwr In
Pwr In	M12	GND	VCCAUX	R6	Pwr In
Pwr In	M14	GND	VCCAUX	U11	Pwr In
Pwr In	N11	GND	VCCAUX	V6	Pwr In
Pwr In	N13	GND			
Pwr In	N17	GND	VCCINT	J10	Pwr In
Pwr In	N2	GND	VCCINT	J12	Pwr In
Pwr In	N21	GND	VCCINT	J14	Pwr In
Pwr In	N9	GND	VCCINT	J8	Pwr In
Pwr In	P10	GND	VCCINT	K11	Pwr In
Pwr In	P12	GND	VCCINT	K13	Pwr In
Pwr In	P14	GND	VCCINT	K9	Pwr In
Pwr In	R18	GND	VCCINT	L10	Pwr In
Pwr In	R5	GND	VCCINT	L12	Pwr In
Pwr In	U2	GND	VCCINT	L14	Pwr In
Pwr In	U21	GND	VCCINT	M11	Pwr In
Pwr In	U7	GND	VCCINT	M13	Pwr In
Pwr In	V10	GND	VCCINT	M9	Pwr In
Pwr In	V14	GND	VCCINT	N10	Pwr In
Pwr In	V4	GND	VCCINT	N12	Pwr In
Pwr In	W16	GND	VCCINT	N14	Pwr In
Pwr In	W19	GND	VCCINT	P11	Pwr In
Pwr In	W7	GND	VCCINT	P13	Pwr In
			VCCINT	P9	Pwr In
			VCCINT	R14	Pwr In
Pwr In	A1	GND			
Pwr In	A22	GND	NC	D12	Unspec
Pwr In	AA13	GND	NC	D13	Unspec
Pwr In	AA17	GND	NC	E10	Unspec
Pwr In	AA5	GND	NC	E12	Unspec
Pwr In	AA9	GND	NC	E14	Unspec
Pwr In	AB1	GND	NC	E8	Unspec
Pwr In	AB22	GND	NC	F10	Unspec
Pwr In	B13	GND	NC	F12	Unspec
Pwr In	B17	GND	NC	F13	Unspec
Pwr In	B5	GND	NC	F14	Unspec
Pwr In	B9	GND	NC	F15	Unspec
Pwr In	D18	GND	NC	F8	Unspec
Pwr In	D4	GND	NC	F9	Unspec
Pwr In	E11	GND	NC	G11	Unspec
Pwr In	E15	GND	NC	G13	Unspec
Pwr In	E2	GND	NC	G8	Unspec
Pwr In	E21	GND	NC	G9	Unspec
Pwr In	E7	GND	NC	H10	Unspec
Pwr In	G18	GND	NC	H11	Unspec
Pwr In	G5	GND	NC	H12	Unspec
Pwr In	H7	GND	NC	H13	Unspec
Pwr In	J11	GND	NC	H14	Unspec
Pwr In	J13	GND	NC	P16	Unspec
Pwr In	J15	GND	NC	R17	Unspec
			NC	P15	Unspec

U?E
XC6SLX45-2FGG484C

FPGA > Xilinx > xc6slx45-2fgg484c

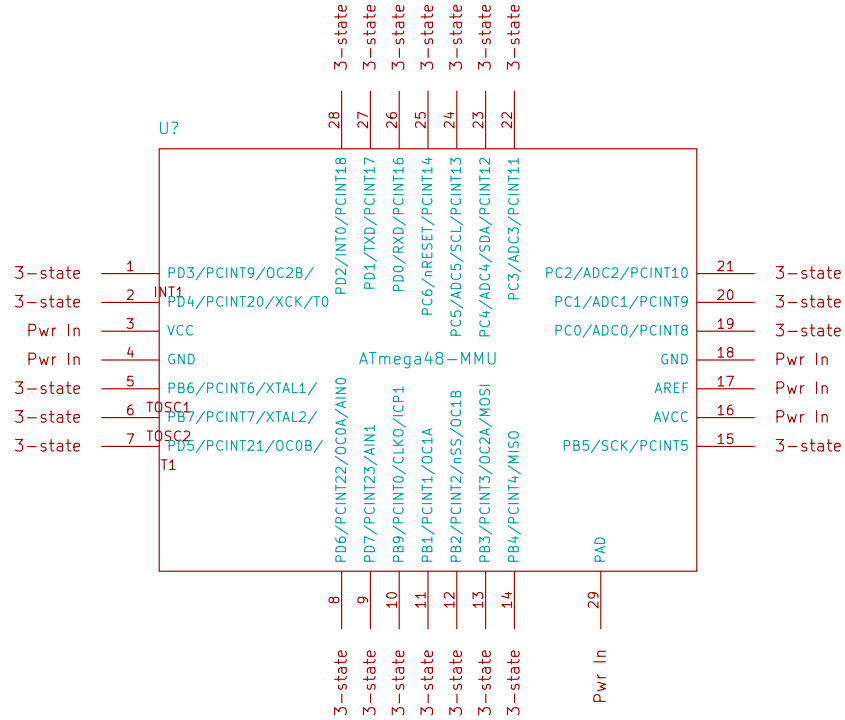
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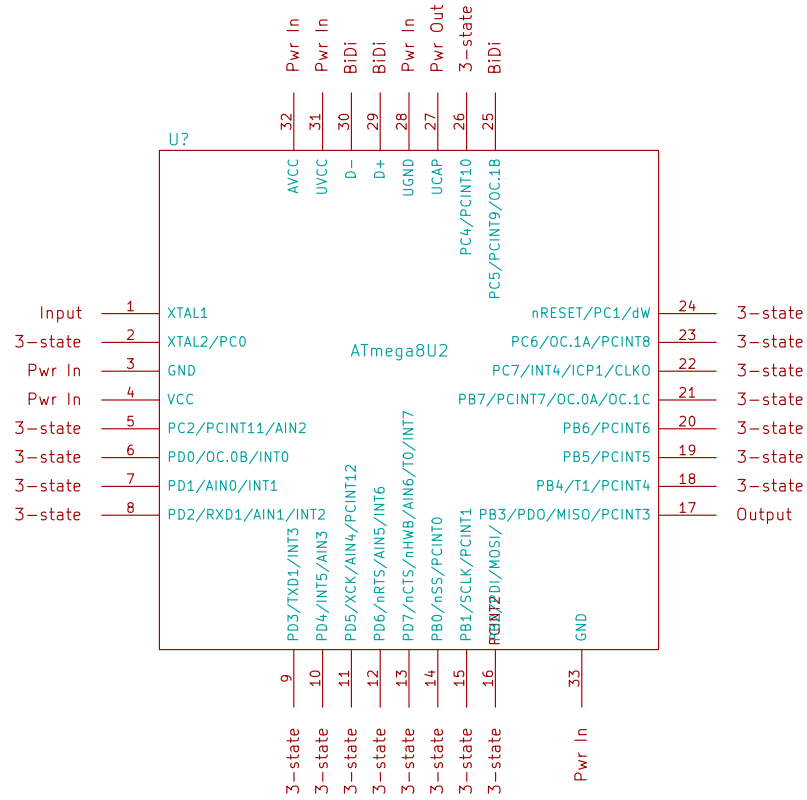
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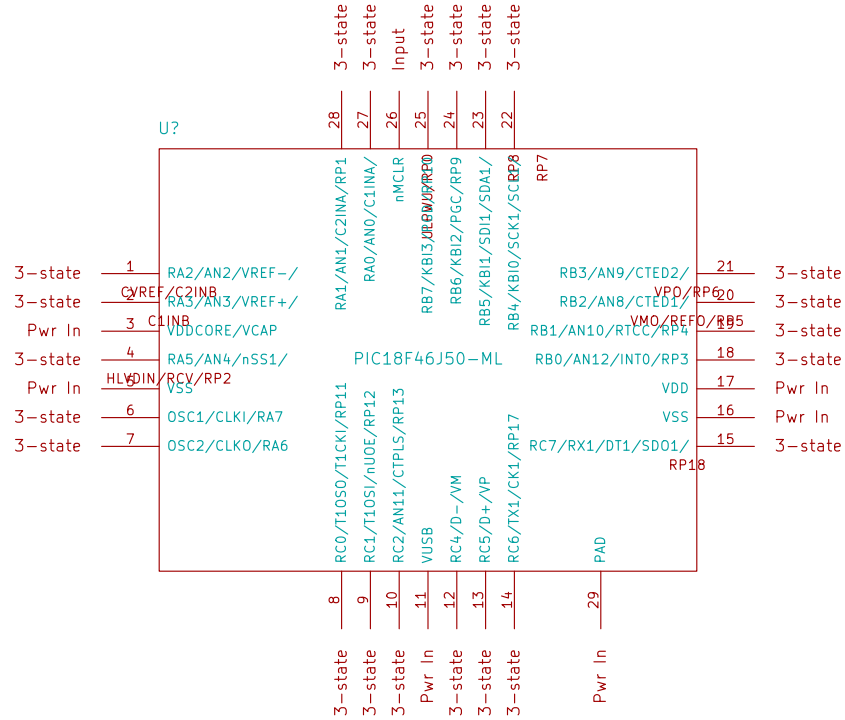
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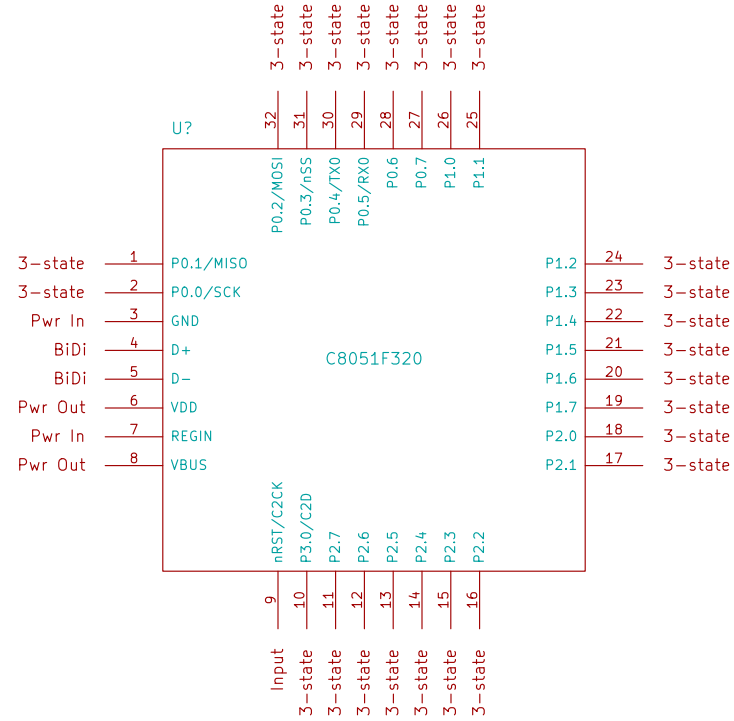
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C8051F320

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