

# SYMBOLS/COMPONENTS

*(for use with KiCad)*

## Qi-Hardware

<http://qi-hardware.com/>

### Git repository:

`git://projects.qi-hardware.com/kicad-libs.git`

### HEAD:

`435a339dce9c23fb9ededfbbbb38c037a7e76724`

`2012-12-26 23:57:27 UTC`

### This catalog:

[http://downloads.qi-hardware.com/people/werner/  
tmp/kicad-libs-components.pdf](http://downloads.qi-hardware.com/people/werner/tmp/kicad-libs-components.pdf)

`2012-12-27 01:17:01 UTC`

1V2	CONN_19	CONN_8X2
1V8	CONN_19X2	CONN_9
2N7002MTF	CONN_1X2	CONN_9X2
2V5	CONN_2	CRYSTAL
3V3	CONN_2	DCJACK_NO
48025-0002	CONN_20	DDCA
4V3	CONN_20X2	DDCC
5V	CONN_21	DDSCHCA
67298-4090	CONN_21X2	DDSCHCC
6N138	CONN_22	DDSCHSER
74320-1004	CONN_22X2	DDSER
74AUP1G08GW,125	CONN_23	DIN_5_2S
74X1G00_5	CONN_23X2	DIODE
74X1G07_4	CONN_24	DIODE-SOT-AXC
74X1G07_5	CONN_24X2	DS
74X1G07_6	CONN_25	DSSCH
74X1G08_5	CONN_25X2	DUALBJT
74X1G08_6	CONN_26	DUALPMOSFET
74X1G125	CONN_26X2	EVQQ7
74X2G126	CONN_27	EZJ-Z0V80010
74X595	CONN_27X2	FILTER
8:10-card	CONN_28	FSMRA2JH
AD1580BRTZ	CONN_28X2	FT232RL
ADV7125KSTZ140	CONN_29	GND
ADV7181C	CONN_29X2	INDUCTOR
ADXL32X	CONN_2X2	IR
ANTENNA	CONN_3	JS28F256J3F105
AP21X2A	CONN_3	KSZ8001L
APX803-40SAG-7	CONN_30	LED
AT86RF230	CONN_30X2	LIS3DH
AT86RF231	CONN_31	LP38511TJ-ADJ
ATmega48-MMU	CONN_31X2	LP38690DT-xx
ATmega8U2	CONN_32	LPC1100-QFN33
ATTINY87-DUAL	CONN_32X2	MCE-100
BALUN	CONN_33	MEMCARD8
BALUN-SMT6	CONN_33X2	MEMCARD8-SHIELD4
BZX84	CONN_34	MEMCARD8-SHIELD4-SW
BZX84	CONN_34X2	MH
C	CONN_35	MIC2090-1YM5
C8051F320	CONN_35X2	MIC2550AYTS
C8051F326	CONN_36	MICRO_USB_B
C_P	CONN_36X2	MINI_USB_B
CMK_A425-206162-001	CONN_37	MOLEX_0480371000
CONN_1	CONN_37X2	MT46V32M16P
CONN_10	CONN_38	NMOSFET-GSD
CONN_10X2	CONN_38X2	NPN-SOT-BEC
CONN_10X2	CONN_39	OPTO-TRANS-2x4
CONN_11	CONN_39X2	OPTO-TRANS-2x4R
CONN_11X2	CONN_3X2	OPTO-TRANS-4
CONN_12	CONN_4	OPTO-TRANS-8
CONN_12X2	CONN_4	OSCILLATOR-VDD-4
CONN_13	CONN_40	PIC18F46J50-ML
CONN_13X2	CONN_40X2	PMOSFET-GSD
CONN_14	CONN_4X2	PNP-SOT-BEC
CONN_14X2	CONN_5	POWERED
CONN_15	CONN_5X2	PPTC
CONN_15X2	CONN_5X2	PTH04000WAH
CONN_16	CONN_6	R
CONN_16X2	CONN_6X2	R_PACK4
CONN_17	CONN_7	RCA_3
CONN_17X2	CONN_7X2	SCJ368R1NUS0B00G
CONN_18	CONN_7X2	SN74LVC1G17DBV
CONN_18X2	CONN_8	SN75HVD12D

SPDT-C  
SW4  
SW\_PUSH  
TC7W04  
TESTPOINT  
TPS6216X  
TPS763xx  
USB-A-PCB  
USB\_A\_PLUG  
VARISTOR  
VGA  
WM9707SCFT/V  
XC6SLX45-2FGG484C  
XC6SLX9-CSG324  
XC6SLXN-TQG144  
XLR-3-F-R  
XLR-3-M-R  
XP01111  
XP01114  
XP01115  
XP01211  
XP01214  
XP01215  
XTAL-4  
ZENER  
ZENER-13  
ZX62-B-5PA

Passive > Basic > C

C

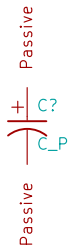
./c.lib

Passive C Passive C?

Passive > Basic > C\_P

C\_P

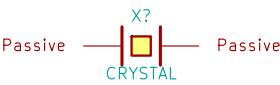
./c.lib



Passive > Basic > crystal

# CRYSTAL

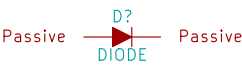
./crystal.lib



Passive > Basic > DIODE

# DIODE

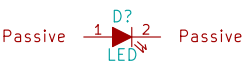
./diode.lib



Passive > Basic > LED

# LED

./led.lib

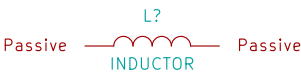




Passive > Basic > INDUCTOR

# INDUCTOR

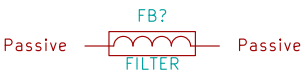
./inductor.lib



Passive > Basic > filter

# FILTER

./filter.lib



Passive > Basic > PPTC

# PPTC

./pptc.lib



Passive > Basic > R

R

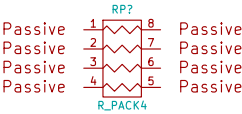
./r.lib



Passive > Basic > r\_pack4

# R\_PACK4

./r.lib



Passive > Basic > VARISTOR

# VARISTOR

./varistor.lib



Passive > Basic > ZENER

# ZENER

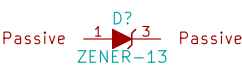
./zener.lib



Passive > Basic > ZENER-13

# ZENER-13

./zener.lib

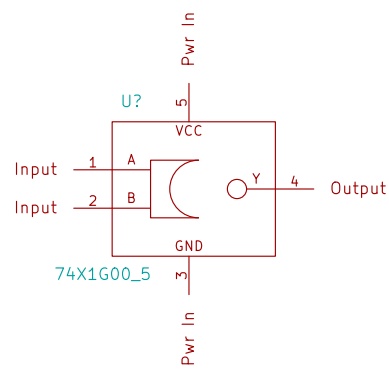




Logic > Single > 74x1g00\_5

# 74X1G00\_5

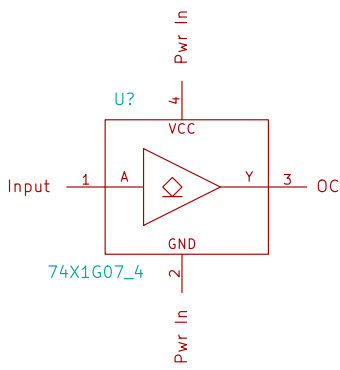
./74x1g00\_5.lib



Logic > Single > 74x1g07\_4

# 74X1G07\_4

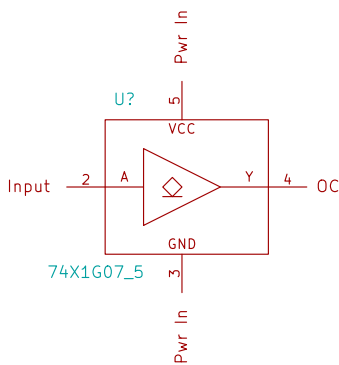
./74x1g07\_4.lib



Logic > Single > 74x1g07\_5

# 74X1G07\_5

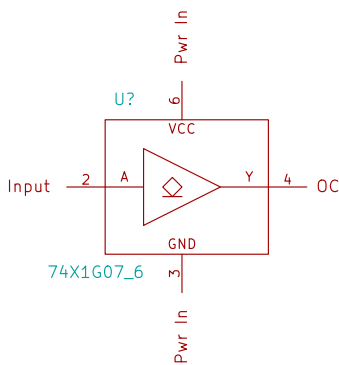
./74x1g07\_5.lib



Logic > Single > 74x1g07\_6

# 74X1G07\_6

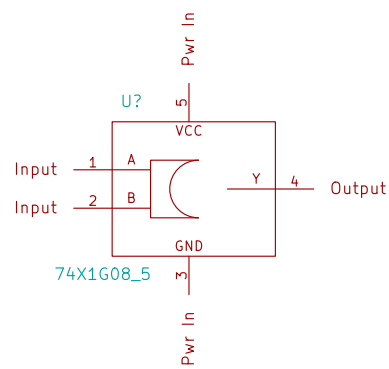
./74x1g07\_6.lib



Logic > Single > 74x1g08\_5

# 74X1G08\_5

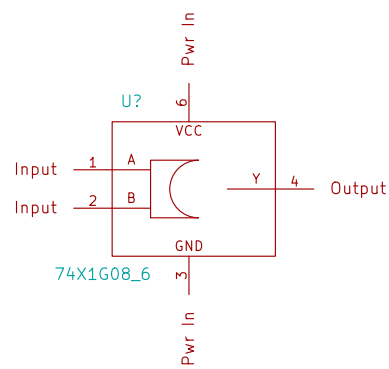
./74x1g08\_5.lib



Logic > Single > 74x1g08\_6

# 74X1G08\_6

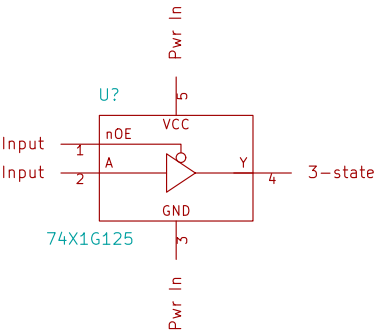
./74x1g08\_6.lib



Logic > Single > 74x1g125

# 74X1G125

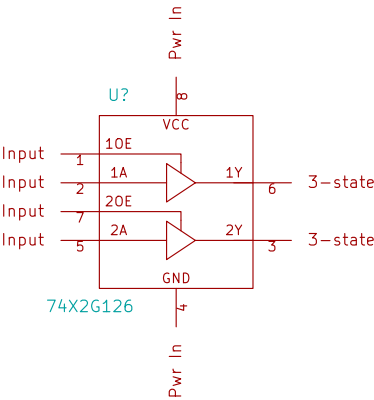
./74x1g125.lib



Logic > Dual > 74x2g126

# 74X2G126

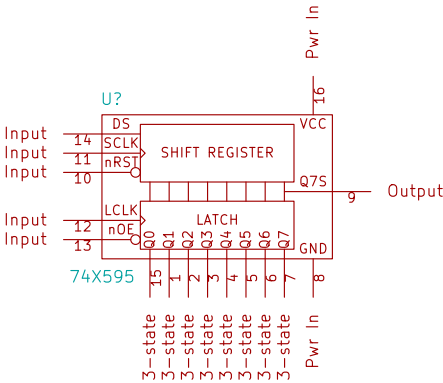
./74x2g126.lib





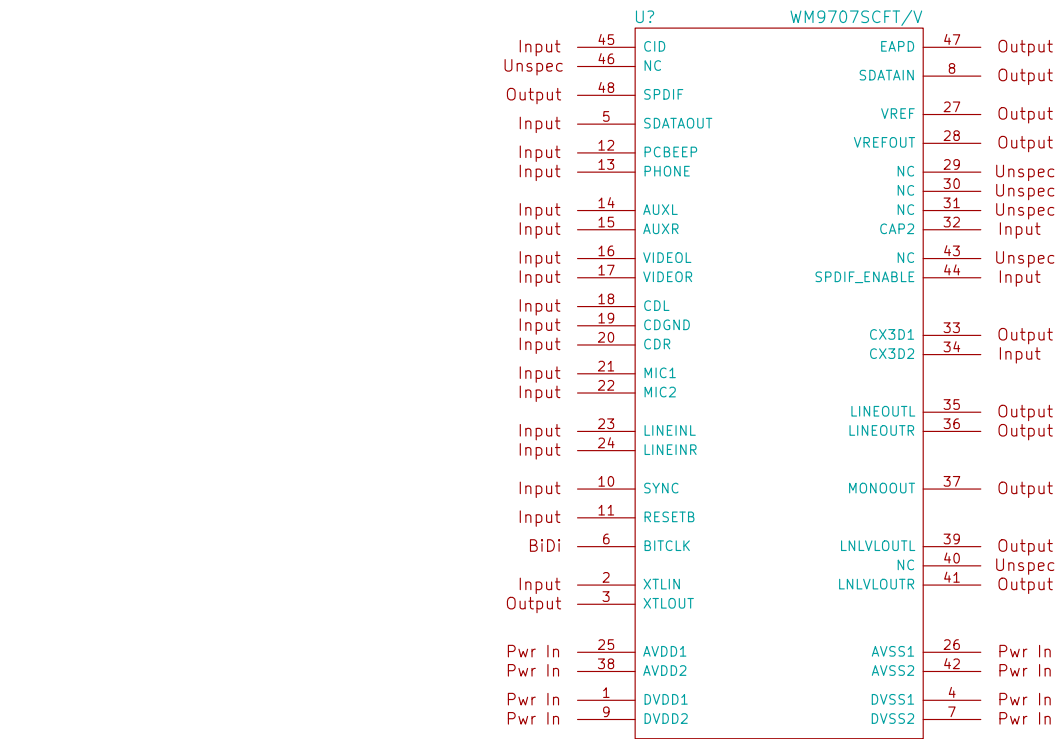
# 74X595

./74x595.lib



# WM9707SCFT/V

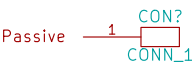
./wolfson.lib



Connector > Generated > conn\_1

# CONN\_1

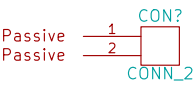
./gencon.lib



Connector > Generated > conn\_2

# CONN\_2

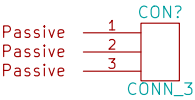
./gencon.lib



Connector > Generated > conn\_3

# CONN\_3

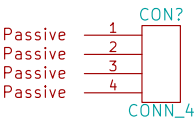
./gencon.lib



Connector > Generated > conn\_4

# CONN\_4

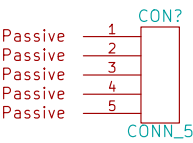
./gencon.lib



Connector > Generated > conn\_5

# CONN\_5

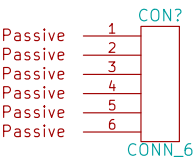
./gencon.lib



Connector > Generated > conn\_6

# CONN\_6

./gencon.lib

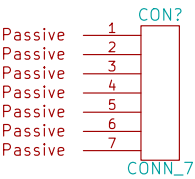




Connector > Generated > conn\_7

# CONN\_7

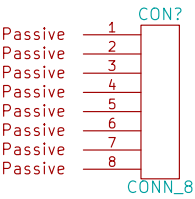
./gencon.lib



Connector > Generated > conn\_8

# CONN\_8

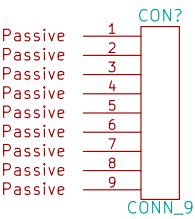
./gencon.lib



Connector > Generated > conn\_9

# CONN\_9

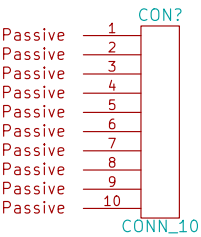
./gencon.lib



Connector > Generated > conn\_10

# CONN\_10

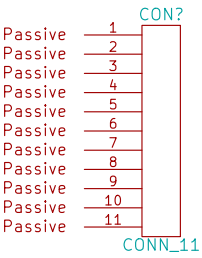
./gencon.lib



Connector > Generated > conn\_11

# CONN\_11

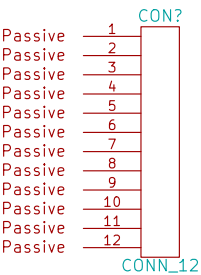
./gencon.lib



Connector > Generated > conn\_12

# CONN\_12

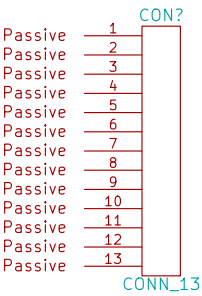
./gencon.lib



Connector > Generated > conn\_13

# CONN\_13

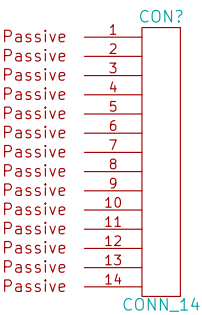
./gencon.lib



Connector > Generated > conn\_14

# CONN\_14

./gencon.lib

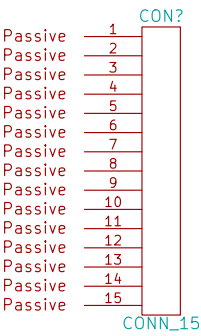




Connector > Generated > conn\_15

# CONN\_15

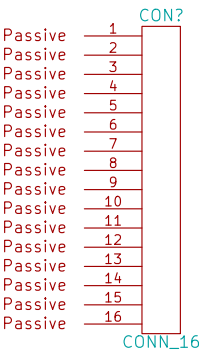
./gencon.lib



Connector > Generated > conn\_16

# CONN\_16

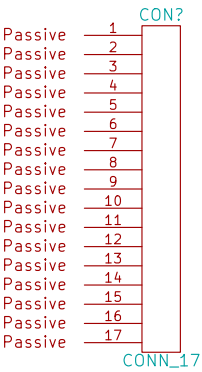
./gencon.lib



Connector > Generated > conn\_17

# CONN\_17

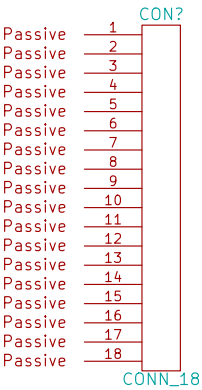
./gencon.lib



Connector > Generated > conn\_18

# CONN\_18

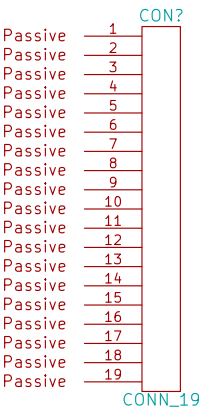
./gencon.lib



Connector > Generated > conn\_19

# CONN\_19

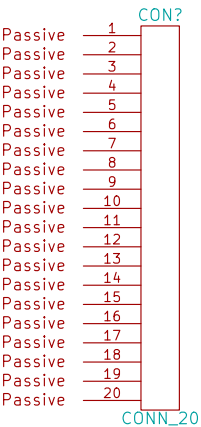
./gencon.lib



Connector > Generated > conn\_20

# CONN\_20

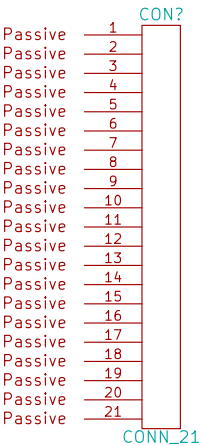
./gencon.lib



Connector > Generated > conn\_21

# CONN\_21

./gencon.lib



Connector > Generated > conn\_22

# CONN\_22

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
		CONN_22



Connector > Generated > conn\_23

# CONN\_23

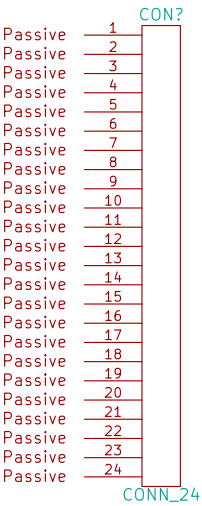
./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
		CONN_23

Connector > Generated > conn\_24

# CONN\_24

./gencon.lib



Connector > Generated > conn\_25

# CONN\_25

./gencon.lib

		1	CON?
Passive	—	2	
Passive	—	3	
Passive	—	4	
Passive	—	5	
Passive	—	6	
Passive	—	7	
Passive	—	8	
Passive	—	9	
Passive	—	10	
Passive	—	11	
Passive	—	12	
Passive	—	13	
Passive	—	14	
Passive	—	15	
Passive	—	16	
Passive	—	17	
Passive	—	18	
Passive	—	19	
Passive	—	20	
Passive	—	21	
Passive	—	22	
Passive	—	23	
Passive	—	24	
Passive	—	25	CONN_25

Connector > Generated > conn\_26

# CONN\_26

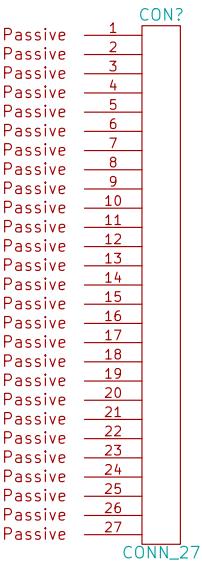
./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
		CONN_26

Connector > Generated > conn\_27

# CONN\_27

./gencon.lib



Connector > Generated > conn\_28

# CONN\_28

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
		CONN_28

Connector > Generated > conn\_29

# CONN\_29

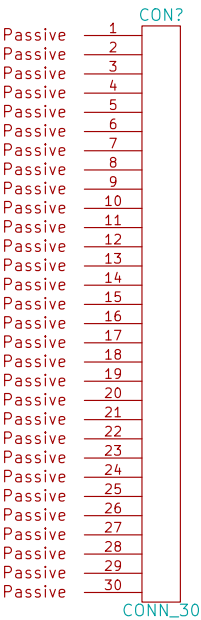
./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
		CONN_29

Connector > Generated > conn\_30

# CONN\_30

./gencon.lib





Connector > Generated > conn\_31

# CONN\_31

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
		CONN_31

Connector > Generated > conn\_32

# CONN\_32

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
		CONN_32

Connector > Generated > conn\_33

# CONN\_33

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
		CONN_33

Connector > Generated > conn\_34

# CONN\_34

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
Passive	34	
		CONN_34

Connector > Generated > conn\_35

# CONN\_35

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
Passive	34	
Passive	35	
		CONN_35

Connector > Generated > conn\_36

# CONN\_36

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
Passive	34	
Passive	35	
Passive	36	
CONN_36		

Connector > Generated > conn\_37

# CONN\_37

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
Passive	34	
Passive	35	
Passive	36	
Passive	37	
		CONN_37

Connector > Generated > conn\_38

# CONN\_38

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
Passive	34	
Passive	35	
Passive	36	
Passive	37	
Passive	38	
		CONN_38



Connector > Generated > conn\_39

# CONN\_39

./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
Passive	34	
Passive	35	
Passive	36	
Passive	37	
Passive	38	
Passive	39	
		CONN_39

Connector > Generated > conn\_40

# CONN\_40

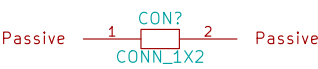
./gencon.lib

		CON?
Passive	1	<div></div>
Passive	2	
Passive	3	
Passive	4	
Passive	5	
Passive	6	
Passive	7	
Passive	8	
Passive	9	
Passive	10	
Passive	11	
Passive	12	
Passive	13	
Passive	14	
Passive	15	
Passive	16	
Passive	17	
Passive	18	
Passive	19	
Passive	20	
Passive	21	
Passive	22	
Passive	23	
Passive	24	
Passive	25	
Passive	26	
Passive	27	
Passive	28	
Passive	29	
Passive	30	
Passive	31	
Passive	32	
Passive	33	
Passive	34	
Passive	35	
Passive	36	
Passive	37	
Passive	38	
Passive	39	
Passive	40	
		CONN_40

Connector > Generated > conn\_1x2

# CONN\_1X2

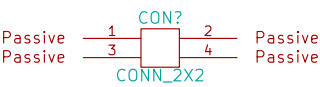
./gencon.lib



Connector > Generated > conn\_2x2

# CONN\_2X2

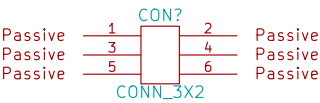
./gencon.lib



Connector > Generated > conn\_3x2

# CONN\_3X2

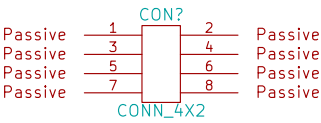
./gencon.lib



Connector > Generated > conn\_4x2

# CONN\_4X2

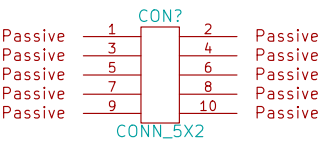
./gencon.lib



Connector > Generated > conn\_5x2

# CONN\_5X2

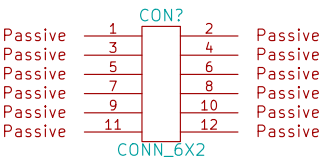
./gencon.lib



Connector > Generated > conn\_6x2

# CONN\_6X2

./gencon.lib

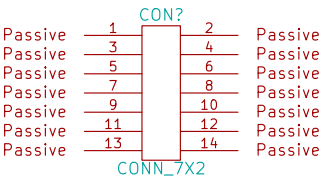




Connector > Generated > conn\_7x2

# CONN\_7X2

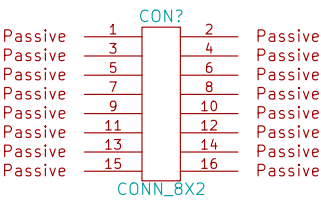
./gencon.lib



Connector > Generated > conn\_8x2

# CONN\_8X2

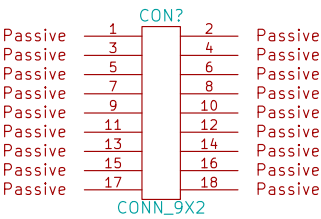
./gencon.lib



Connector > Generated > conn\_9x2

# CONN\_9X2

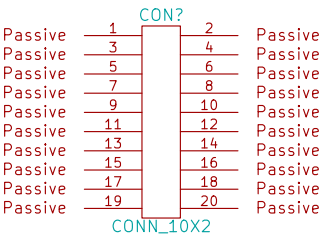
./gencon.lib



Connector > Generated > conn\_10x2

# CONN\_10X2

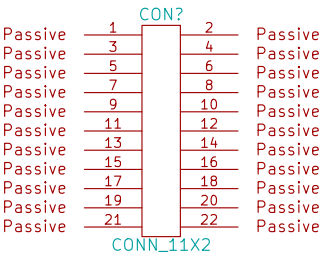
./gencon.lib



Connector > Generated > conn\_11x2

# CONN\_11X2

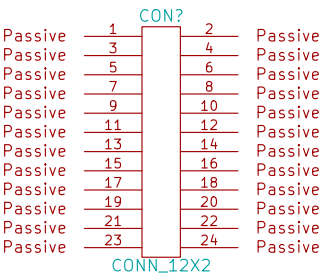
./gencon.lib



Connector > Generated > conn\_12x2

# CONN\_12X2

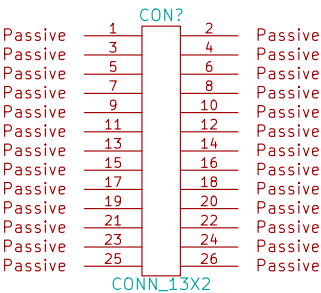
./gencon.lib



Connector > Generated > conn\_13x2

# CONN\_13X2

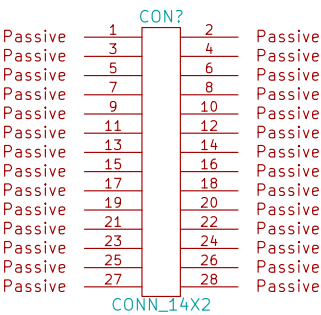
./gencon.lib



Connector > Generated > conn\_14x2

# CONN\_14X2

./gencon.lib

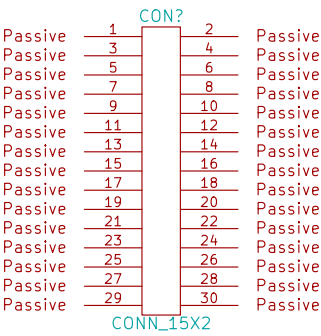




Connector > Generated > conn\_15x2

# CONN\_15X2

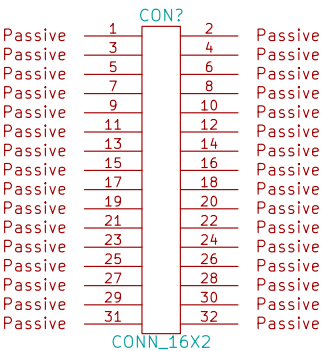
./gencon.lib



Connector > Generated > conn\_16x2

# CONN\_16X2

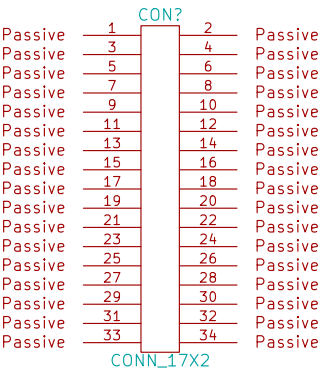
./gencon.lib



Connector > Generated > conn\_17x2

# CONN\_17X2

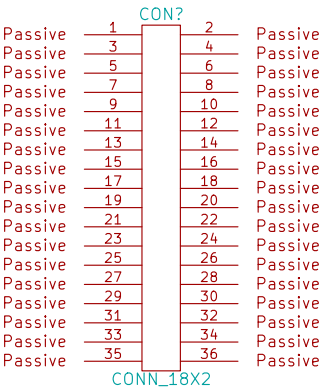
./gencon.lib



Connector > Generated > conn\_18x2

# CONN\_18X2

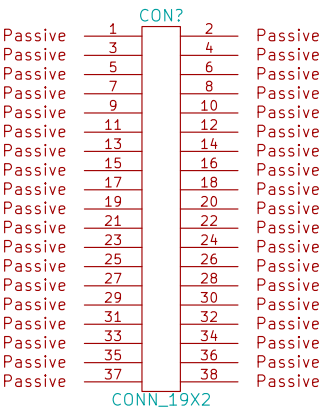
./gencon.lib



Connector > Generated > conn\_19x2

# CONN\_19X2

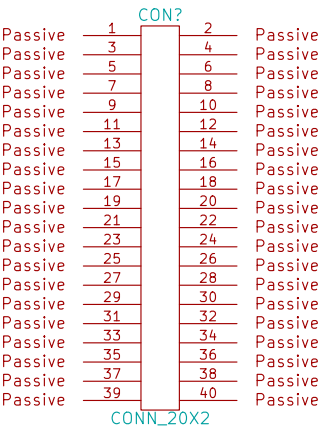
./gencon.lib



Connector > Generated > conn\_20x2

# CONN\_20X2

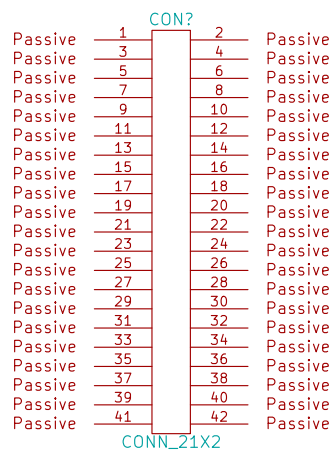
./gencon.lib



## Connector > Generated > conn\_21x2

# CONN\_21X2

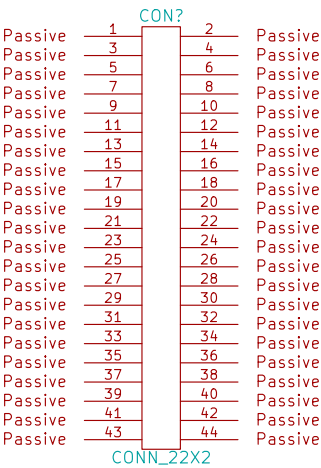
```
./gencon.lib
```



Connector > Generated > conn\_22x2

# CONN\_22X2

./gencon.lib

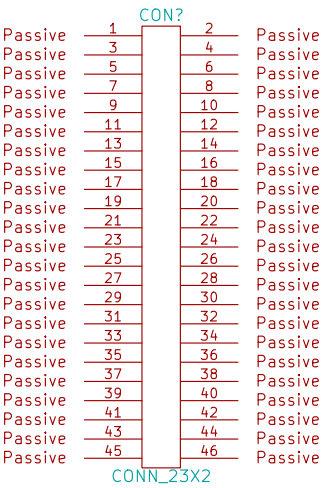




Connector > Generated > conn\_23x2

# CONN\_23X2

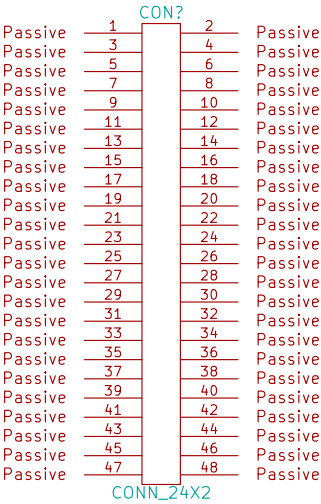
./gencon.lib



Connector > Generated > conn\_24x2

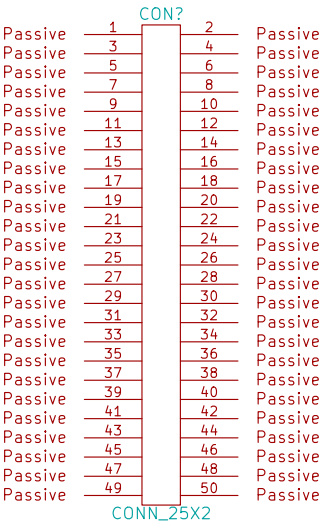
# CONN\_24X2

./gencon.lib



# CONN\_25X2

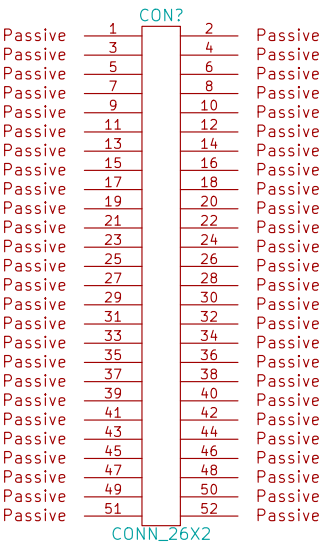
./gencon.lib



Connector > Generated > conn\_26x2

# CONN\_26X2

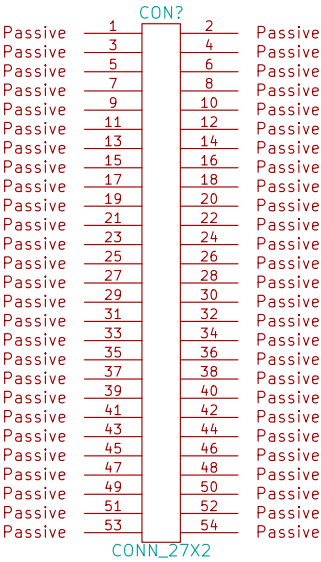
./gencon.lib



Connector > Generated > conn\_27x2

# CONN\_27X2

./gencon.lib



Connector > Generated > conn\_28x2

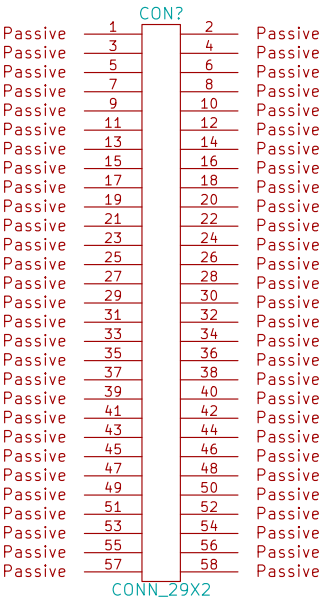
# CONN\_28X2

./gencon.lib

		CON?		
Passive	1		2	Passive
Passive	3		4	Passive
Passive	5		6	Passive
Passive	7		8	Passive
Passive	9		10	Passive
Passive	11		12	Passive
Passive	13		14	Passive
Passive	15		16	Passive
Passive	17		18	Passive
Passive	19		20	Passive
Passive	21		22	Passive
Passive	23		24	Passive
Passive	25		26	Passive
Passive	27		28	Passive
Passive	29		30	Passive
Passive	31		32	Passive
Passive	33		34	Passive
Passive	35		36	Passive
Passive	37		38	Passive
Passive	39		40	Passive
Passive	41		42	Passive
Passive	43		44	Passive
Passive	45		46	Passive
Passive	47		48	Passive
Passive	49		50	Passive
Passive	51		52	Passive
Passive	53		54	Passive
Passive	55		56	Passive
		CONN_28X2		

# CONN\_29X2

./gencon.lib



Connector > Generated > conn\_30x2

# CONN\_30X2

./gencon.lib

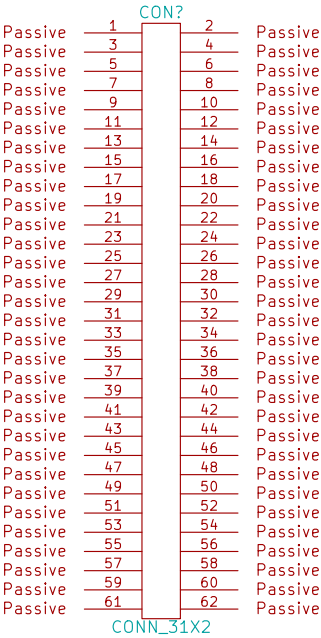
		CON?		
Passive	1		2	Passive
Passive	3		4	Passive
Passive	5		6	Passive
Passive	7		8	Passive
Passive	9		10	Passive
Passive	11		12	Passive
Passive	13		14	Passive
Passive	15		16	Passive
Passive	17		18	Passive
Passive	19		20	Passive
Passive	21		22	Passive
Passive	23		24	Passive
Passive	25		26	Passive
Passive	27		28	Passive
Passive	29		30	Passive
Passive	31		32	Passive
Passive	33		34	Passive
Passive	35		36	Passive
Passive	37		38	Passive
Passive	39		40	Passive
Passive	41		42	Passive
Passive	43		44	Passive
Passive	45		46	Passive
Passive	47		48	Passive
Passive	49		50	Passive
Passive	51		52	Passive
Passive	53		54	Passive
Passive	55		56	Passive
Passive	57		58	Passive
Passive	59		60	Passive
		CONN_30X2		



Connector > Generated > conn\_31x2

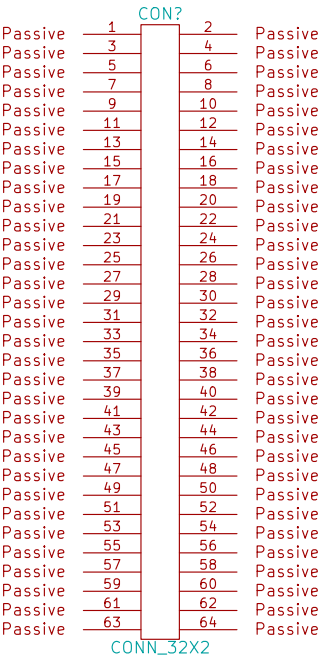
# CONN\_31X2

./gencon.lib



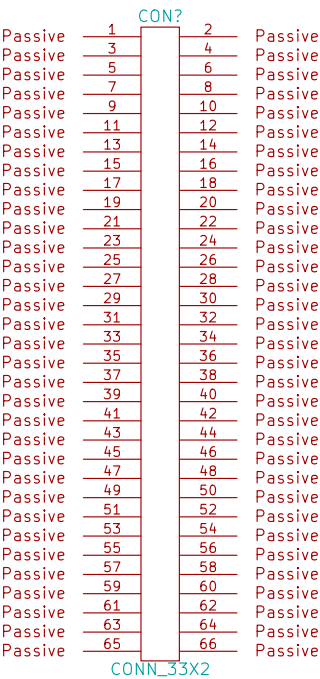
# CONN\_32X2

./gencon.lib



# CONN\_33X2

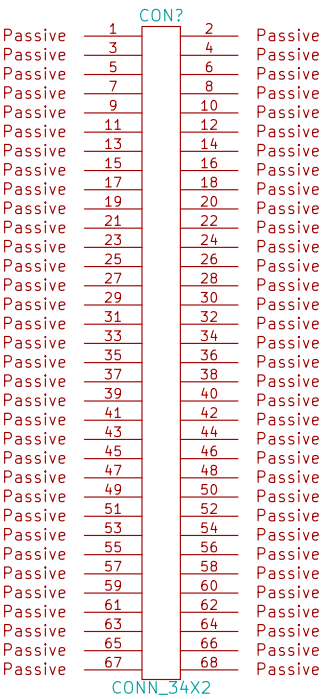
./gencon.lib



Connector > Generated > conn\_34x2

# CONN\_34X2

./gencon.lib



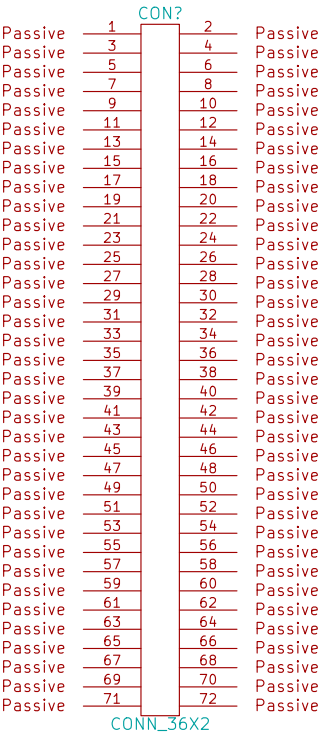
# CONN\_35X2

./gencon.lib

		CON?		
Passive	1		2	Passive
Passive	3		4	Passive
Passive	5		6	Passive
Passive	7		8	Passive
Passive	9		10	Passive
Passive	11		12	Passive
Passive	13		14	Passive
Passive	15		16	Passive
Passive	17		18	Passive
Passive	19		20	Passive
Passive	21		22	Passive
Passive	23		24	Passive
Passive	25		26	Passive
Passive	27		28	Passive
Passive	29		30	Passive
Passive	31		32	Passive
Passive	33		34	Passive
Passive	35		36	Passive
Passive	37		38	Passive
Passive	39		40	Passive
Passive	41		42	Passive
Passive	43		44	Passive
Passive	45		46	Passive
Passive	47		48	Passive
Passive	49		50	Passive
Passive	51		52	Passive
Passive	53		54	Passive
Passive	55		56	Passive
Passive	57		58	Passive
Passive	59		60	Passive
Passive	61		62	Passive
Passive	63		64	Passive
Passive	65		66	Passive
Passive	67		68	Passive
Passive	69		70	Passive
		CONN_35X2		

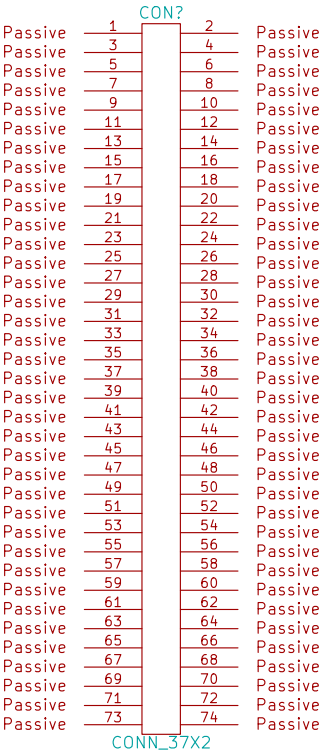
# CONN\_36X2

./gencon.lib



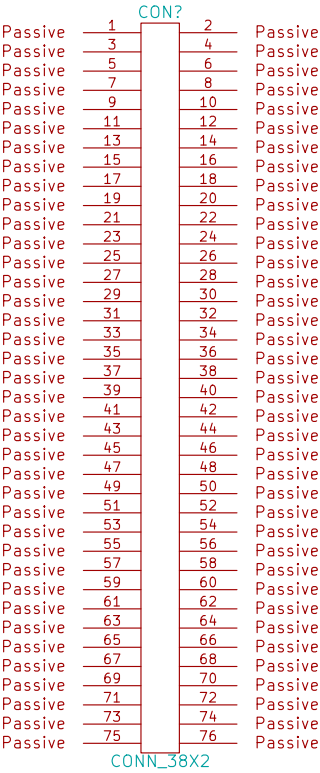
# CONN\_37X2

./gencon.lib



# CONN\_38X2

./gencon.lib





# CONN\_39X2

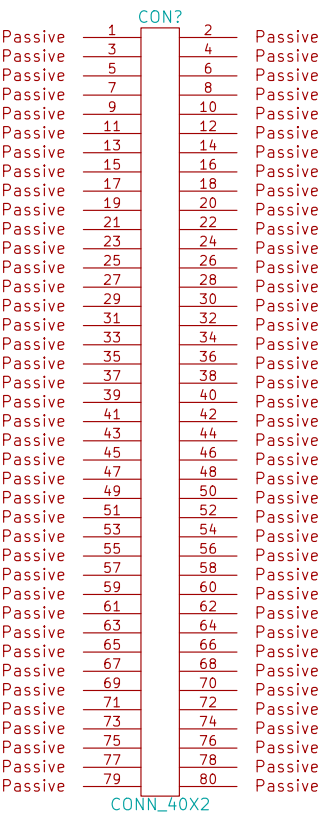
./gencon.lib

	CON?		
Passive	1	2	Passive
Passive	3	4	Passive
Passive	5	6	Passive
Passive	7	8	Passive
Passive	9	10	Passive
Passive	11	12	Passive
Passive	13	14	Passive
Passive	15	16	Passive
Passive	17	18	Passive
Passive	19	20	Passive
Passive	21	22	Passive
Passive	23	24	Passive
Passive	25	26	Passive
Passive	27	28	Passive
Passive	29	30	Passive
Passive	31	32	Passive
Passive	33	34	Passive
Passive	35	36	Passive
Passive	37	38	Passive
Passive	39	40	Passive
Passive	41	42	Passive
Passive	43	44	Passive
Passive	45	46	Passive
Passive	47	48	Passive
Passive	49	50	Passive
Passive	51	52	Passive
Passive	53	54	Passive
Passive	55	56	Passive
Passive	57	58	Passive
Passive	59	60	Passive
Passive	61	62	Passive
Passive	63	64	Passive
Passive	65	66	Passive
Passive	67	68	Passive
Passive	69	70	Passive
Passive	71	72	Passive
Passive	73	74	Passive
Passive	75	76	Passive
Passive	77	78	Passive
	CONN_39X2		

Connector > Generated > conn\_40x2

# CONN\_40X2

./gencon.lib



Connector > Generic > TESTPOINT

# TESTPOINT

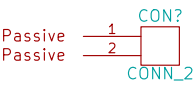
./testpoint.lib

Passive — 

Connector > Generic > conn\_2

# CONN\_2

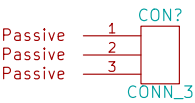
./gencon.lib



Connector > Generic > conn\_3

# CONN\_3

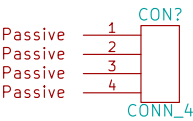
./gencon.lib



Connector > Generic > conn\_4

# CONN\_4

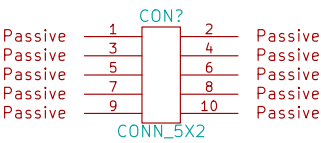
./gencon.lib



Connector > Generic > conn\_5x2

# CONN\_5X2

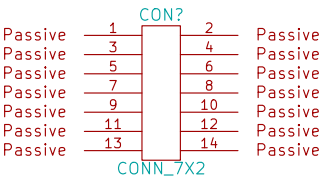
./gencon.lib



Connector > Generic > conn\_7x2

# CONN\_7X2

./gencon.lib

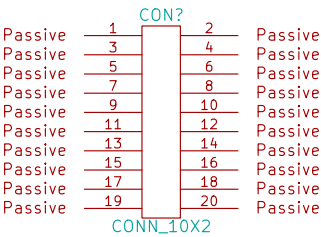




Connector > Generic > conn\_10x2

# CONN\_10X2

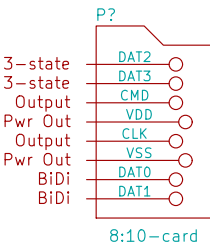
./gencon.lib



Connector > Card > 8:10-card

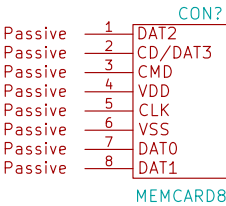
# 8:10-card

./8\_10-card.lib



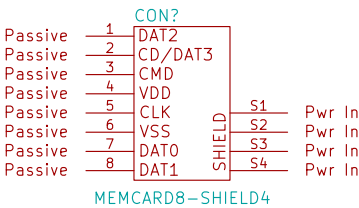
# MEMCARD8

./con.lib



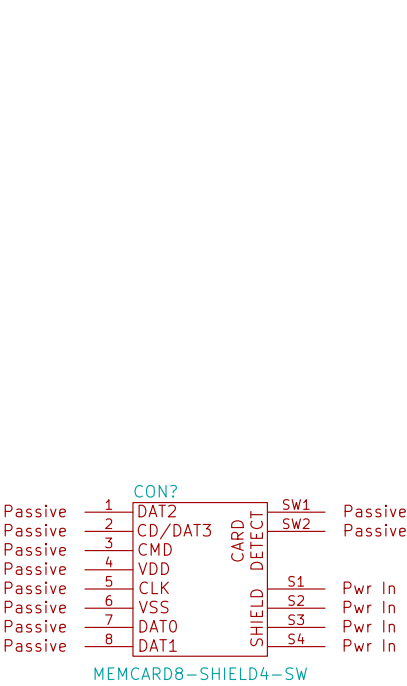
# MEMCARD8-SHIELD4

./memcard8.lib



# MEMCARD8-SHIELD4-SW

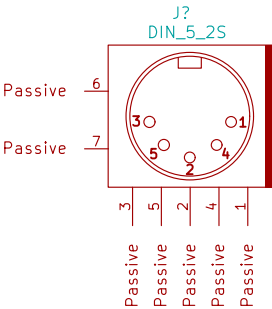
./memcard8.lib



Connector > Circular > DIN\_5\_2S

# DIN\_5\_2S

./din\_5\_2s.lib



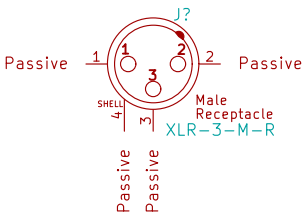
671-0500, SOCKET, DIN, PCB, 180, 5PIN, 5WAYS, 45 DEGREE  
DIN\_5\_2S  
<http://www.farnell.com/datasheets/65433.pdf>



Connector > Circular > XLR-3-M-R

# XLR-3-M-R

./xlr-3.lib



NC3MAH, 3 pole male XLR receptacle, grounding: separate ground contact to mating connector shell and from XLR

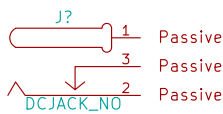
[http://downloads.qj-hardware.com/hardware/milkymist\\_one/datasheet/MIDI/nc3mah-2.pdf](http://downloads.qj-hardware.com/hardware/milkymist_one/datasheet/MIDI/nc3mah-2.pdf)



Connector > Power > DCJACK\_NO

# DCJACK\_NO

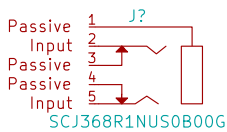
./dc\_jack.lib



Connector > Audio > SCJ368R1NUS0B00G

# SCJ368R1NUS0B00G

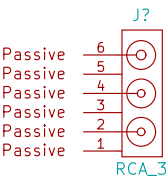
./phonejack.lib



Connector > Video > rca\_3

# RCA\_3

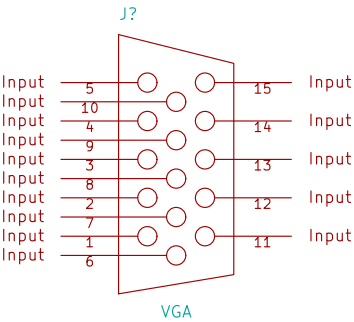
./rca.lib



Connector > Video > vga

# VGA

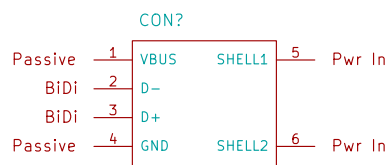
./vga.lib



Connector > USB > usb\_a\_plug

# USB\_A\_PLUG, MOLEX\_0480371000

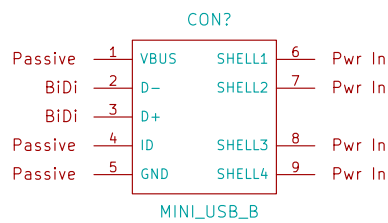
./usb\_a\_plug.lib



Connector > USB > mini\_usb\_b

MINI\_USB\_B, CMK\_A425-206162-001

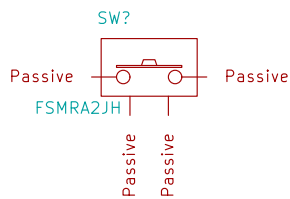
./mini\_usb\_b.lib



Electromechanical > Switch > FSMRA2JH

# FSMRA2JH

./switch.lib



FSMRA2JH, SWITCH, TACTILE, SPNO, R/A, THROUGH HOLE  
TACTILE SW

<http://www.farnell.com/datasheets/51343.pdf>

Electromechanical > Switch > SW\_PUSH

# SW\_PUSH

./switch.lib

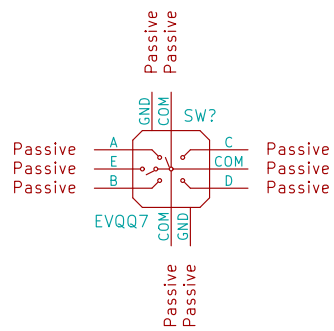




Electromechanical &gt; Switch &gt; EVQQ7

# EVQQ7

```
./evqq7.lib
```



Electromechanical > Case > MH

# MH

./hole.lib



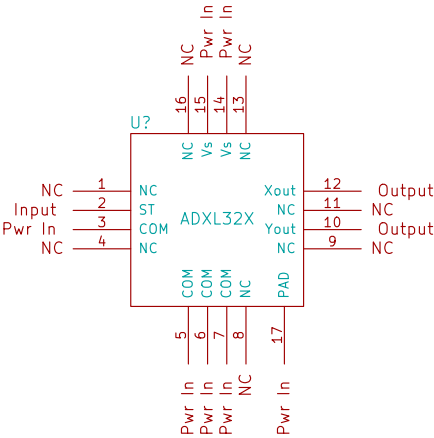
Mechanical Hole with 1 contact

MH\_1

Sensor > Acceleration > ADXL32X

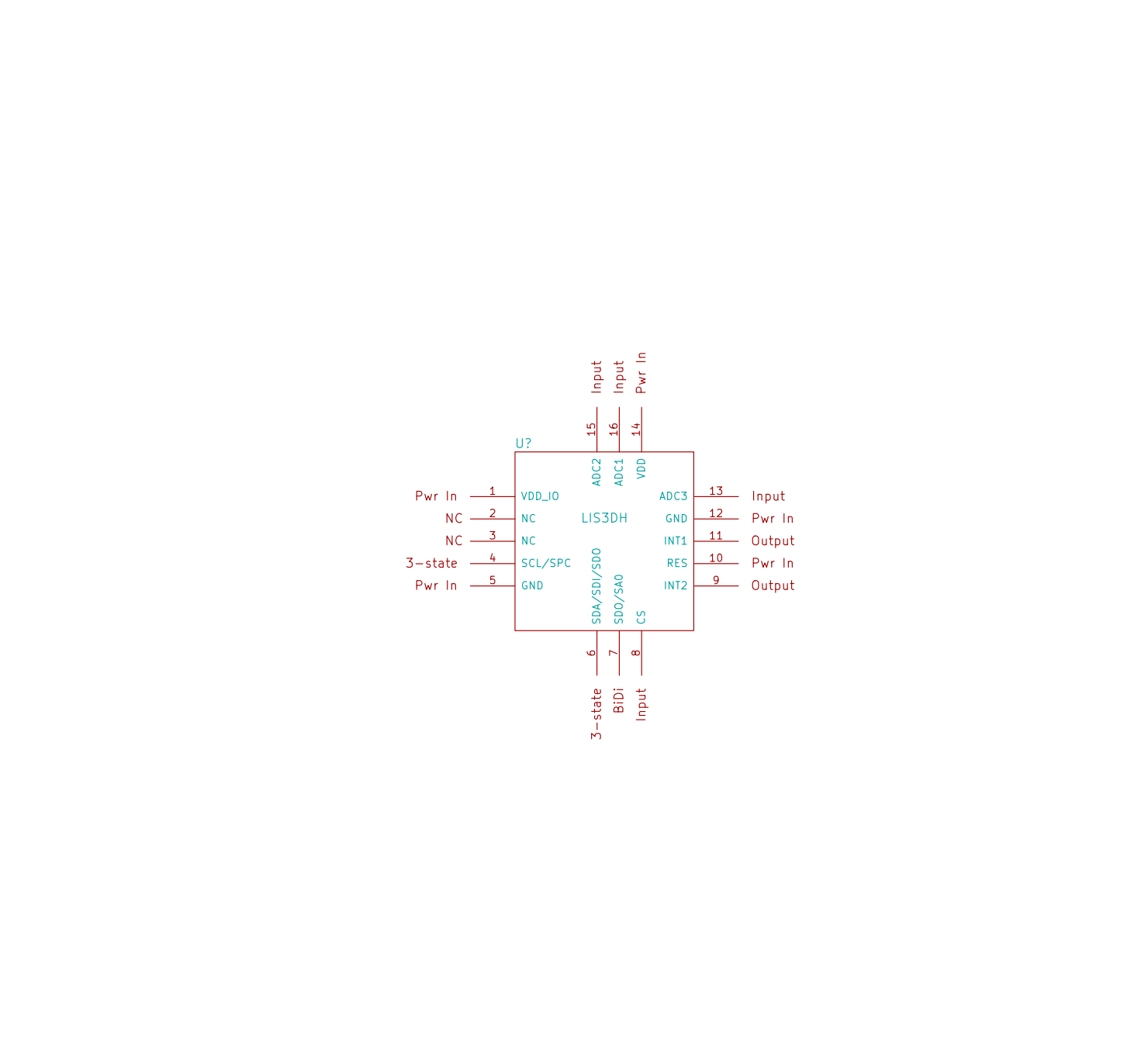
ADXL32X

./adx132x.lib



# LIS3DH

./lis3dh.lib



# XC6SLXN-TQG144 (A)

./xc6slx9-tqg144.lib

BiDi	<u>144</u>	IO_L1P_HSWAPEN_0	
BiDi	<u>143</u>	IO_L1N_VREF_0	
BiDi	<u>142</u>	IO_L2P_0	
BiDi	<u>141</u>	IO_L2N_0	
BiDi	<u>140</u>	IO_L3P_0	
BiDi	<u>139</u>	IO_L3N_0	
BiDi	<u>138</u>	IO_L4P_0	
BiDi	<u>137</u>	IO_L4N_0	
BiDi	<u>134</u>	IO_L34P_GCLK19_0	
BiDi	<u>133</u>	IO_L34N_GCLK18_0	
BiDi	<u>132</u>	IO_L35P_GCLK17_0	
BiDi	<u>131</u>	IO_L35N_GCLK16_0	
BiDi	<u>127</u>	IO_L36P_GCLK15_0	
BiDi	<u>126</u>	IO_L36N_GCLK14_0	
BiDi	<u>124</u>	IO_L37P_GCLK13_0	
BiDi	<u>123</u>	IO_L37N_GCLK12_0	
BiDi	<u>121</u>	IO_L62P_0	
BiDi	<u>120</u>	IO_L62N_VREF_0	
BiDi	<u>119</u>	IO_L63P_SCP7_0	
BiDi	<u>118</u>	IO_L63N_SCP6_0	
BiDi	<u>117</u>	IO_L64P_SCP5_0	
BiDi	<u>116</u>	IO_L64N_SCP4_0	
BiDi	<u>115</u>	IO_L65P_SCP3_0	
BiDi	<u>114</u>	IO_L65N_SCP2_0	
BiDi	<u>112</u>	IO_L66P_SCP1_0	
BiDi	<u>111</u>	IO_L66N_SCP0_0	

U?A

XC6SLXN-TQG144

# XC6SLXN-TQG144 (B)

./xc6slx9-tqg144.lib

BiDi	<u>105</u>	IO_L1P_1
BiDi	<u>104</u>	IO_L1N_VREF_1
BiDi	<u>102</u>	IO_L32P_1
BiDi	<u>101</u>	IO_L32N_1
BiDi	<u>100</u>	IO_L33P_1
BiDi	<u>99</u>	IO_L33N_1
BiDi	<u>98</u>	IO_L34P_1
BiDi	<u>97</u>	IO_L34N_1
BiDi	<u>95</u>	IO_L40P_GCLK11_1
BiDi	<u>94</u>	IO_L40N_GCLK10_1
BiDi	<u>93</u>	IO_L41P_GCLK9_IRDY1_1
BiDi	<u>92</u>	IO_L41N_GCLK8_1
BiDi	<u>88</u>	IO_L42P_GCLK7_1
BiDi	<u>87</u>	IO_L42N_GCLK6_TRDY1_1
BiDi	<u>85</u>	IO_L43P_GCLK5_1
BiDi	<u>84</u>	IO_L43N_GCLK4_1
BiDi	<u>83</u>	IO_L45P_1
BiDi	<u>82</u>	IO_L45N_1
BiDi	<u>81</u>	IO_L46P_1
BiDi	<u>80</u>	IO_L46N_1
BiDi	<u>79</u>	IO_L47P_1
BiDi	<u>78</u>	IO_L47N_1
BiDi	<u>75</u>	IO_L74P_AWAKE_1
BiDi	<u>74</u>	IO_L74N_DOUT_BUSY_1
		U?B
		XC6SLXN-TQG144

# XC6SLXN-TQG144 (C)

./xc6slx9-tqg144.lib

NC	72	CMPCS_B_2	
Output	71	DONE_2	
BiDi	70	IO_L1P_CCLK_2	
BiDi	69	IO_L1N_M0_CMPMISO_2	
BiDi	67	IO_L2P_CMLPCLK_2	
BiDi	66	IO_L2N_CMPMOSI_2	
BiDi	65	IO_L3P_D0_DIN_MISO_MISO1_2	
BiDi	64	IO_L3N_MOSLCSLB_MISO0_2	
BiDi	62	IO_L12P_D1_MISO2_2	
BiDi	61	IO_L12N_D2_MISO3_2	
BiDi	60	IO_L13P_M1_2	
BiDi	59	IO_L13N_D10_2	
BiDi	58	IO_L14P_D11_2	
BiDi	57	IO_L14N_D12_2	
BiDi	56	IO_L30P_GCLK1_D13_2	
BiDi	55	IO_L30N_GCLK0_USERCCLK_2	
BiDi	51	IO_L31P_GCLK31_D14_2	
BiDi	50	IO_L31N_GCLK30_D15_2	
BiDi	48	IO_L48P_D7_2	
BiDi	47	IO_L48N_RDWR_B_VREF_2	
BiDi	46	IO_L49P_D3_2	
BiDi	45	IO_L49N_D4_2	
BiDi	44	IO_L62P_D5_2	
BiDi	43	IO_L62N_D6_2	
BiDi	41	IO_L64P_D8_2	
BiDi	40	IO_L64N_D9_2	
BiDi	39	IO_L65P_INIT_B_2	
BiDi	38	IO_L62N_CS0_B_2	
Input	37	PROGRAM_B_2	

U?C  
XC6SLXN-TQG144

# XC6SLXN-TQG144 (D)

./xc6slx9-tqg144.lib

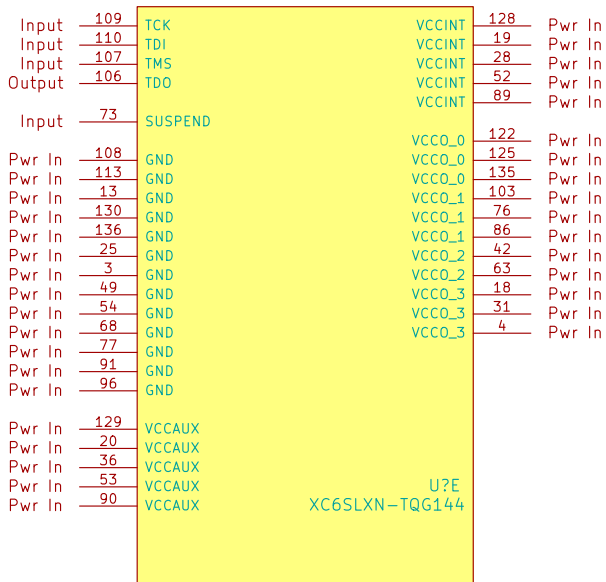
BiDi	35	IOL1P_3	
BiDi	34	IO_L1N_VREF_3	
BiDi	33	IO_L2P_3	
BiDi	32	IO_L2N_3	
BiDi	30	IO_L36P_3	
BiDi	29	IO_L36N_3	
BiDi	27	IO_L37P_3	
BiDi	26	IO_L37N_3	
BiDi	24	IO_L41P_GCLK27_3	
BiDi	23	IO_L41N_GCLK26_3	
BiDi	22	IO_L42P_GCLK25_TRDY2_3	
BiDi	21	IO_L42N_GCLK24_3	
BiDi	17	IO_L43P_GCLK23_3	
BiDi	16	IO_L43N_GCLK22_IRDY2_3	
BiDi	15	IO_L44P_GCLK21_3	
BiDi	14	IO_L44N_GCLK20_3	
BiDi	12	IO_L49P_3	
BiDi	11	IO_L49N_3	
BiDi	10	IO_L50P_3	
BiDi	9	IO_L50N_3	
BiDi	8	IO_L51P_3	
BiDi	7	IO_L51N_3	
BiDi	6	IO_L52P_3	
BiDi	5	IO_L52N_3	
BiDi	2	IO_L83P_3	
BiDi	1	IO_L83N_VREF_3	
			U?D
			XC6SLXN-TQG144



FPGA > Xilinx > XC6SLXN-TQG144

# XC6SLXN-TQG144 (E)

./xc6slx9-tqg144.lib



```
./xc6s01x9-csg3241-1.lib
```

[illegible]

FPGA > Xilinx > xc6slx45-2fgg484c

# XC6SLX45-2FGG484C (A)

./xc6slx45-2fgg484c.lib



FPGA > Xilinx > xc6slx45-2fgg484c

# XC6SLX45-2FGG484C (B)

./xc6slx45-2fgg484c.lib

BiDi	L19	IO_L42N_GCLK6_TRDY1_M1LDM_1	IO_L1P_A25_1	C19	BiDi
BiDi	J20	IO_L43P_GCLK5_M1DQ4_1	IO_L1N_A24_VREF_1	B20	BiDi
BiDi	J22	IO_L43N_GCLK4_M1DQ5_1	IO_L9P_1	G16	BiDi
BiDi	K21	IO_L44P_A3_M1DQ6_1	IO_L9N_1	G17	BiDi
BiDi	K22	IO_L44N_A2_M1DQ7_1	IO_L10P_1	F16	BiDi
BiDi	L20	IO_L45P_A1_M1DQ5_1	IO_L10N_1	F17	BiDi
BiDi	L22	IO_L45N_A0_M1DQ5N_1	IO_L19P_1	B21	BiDi
BiDi	M21	IO_L46P_FCS_B_M1DQ2_1	IO_L19N_1	B22	BiDi
BiDi	M22	IO_L46N_FOE_B_M1DQ3_1	IO_L20P_1	A20	BiDi
BiDi	N20	IO_L47P_FWE_B_M1DQ0_1	IO_L20N_1	A21	BiDi
BiDi	N22	IO_L47N_LDC_M1DQ1_1	IO_L21P_1	K16	BiDi
BiDi	P22	IO_L48N_M1DQ9_1	IO_L21N_1	J16	BiDi
BiDi	P21	IO_L48P_HDC_M1DQ8_1	IO_L28P_1	H16	BiDi
BiDi	R20	IO_L49P_M1DQ10_1	IO_L28N_VREF_1	H17	BiDi
BiDi	R22	IO_L49N_M1DQ11_1	IO_L29P_A23_M1A13_1	D19	BiDi
BiDi	T21	IO_L50P_M1UDQ5_1	IO_L29N_A22_M1A14_1	D20	BiDi
BiDi	T22	IO_L50N_M1UDQ5N_1	IO_L30P_A21_M1RESET_1	F18	BiDi
BiDi	U20	IO_L51P_M1DQ12_1	IO_L30N_A20_M1A11_1	F19	BiDi
BiDi	U22	IO_L51N_M1DQ13_1	IO_L31P_A19_M1CKE_1	D21	BiDi
BiDi	V21	IO_L52P_M1DQ14_1	IO_L31N_A18_M1A12_1	D22	BiDi
BiDi	V22	IO_L52N_M1DQ15_1	IO_L32P_A17_M1A8_1	C20	BiDi
BiDi	M19	IO_L53P_1	IO_L32N_A16_M1A9_1	C22	BiDi
BiDi	N19	IO_L53N_VREF_1	IO_L33P_A15_M1A10_1	G19	BiDi
BiDi	M16	IO_L58P_1	IO_L33N_A14_M1A4_1	F20	BiDi
BiDi	L15	IO_L58N_1	IO_L34P_A13_M1WE_1	H19	BiDi
BiDi	P19	IO_L59P_1	IO_L34N_A12_M1BA2_1	H18	BiDi
BiDi	P20	IO_L59N_1	IO_L35P_A11_M1A7_1	E20	BiDi
BiDi	W20	IO_L60P_1	IO_L35N_A10_M1A2_1	E22	BiDi
BiDi	W22	IO_L60N_1	IO_L36P_A9_M1BA0_1	J17	BiDi
BiDi	L17	IO_L61P_1	IO_L36N_A8_M1BA1_1	K17	BiDi
BiDi	K18	IO_L61N_1	IO_L37P_A7_M1A0_1	F21	BiDi
BiDi	U19	IO_L70P_1	IO_L37N_A6_M1A1_1	F22	BiDi
BiDi	V20	IO_L70N_1	IO_L38P_A5_M1CLK_1	H20	BiDi
BiDi	M17	IO_L71P_1	IO_L38N_A4_M1CLKN_1	J19	BiDi
BiDi	M18	IO_L71N_1	IO_L39P_M1A3_1	G20	BiDi
BiDi	P17	IO_L72P_1	IO_L39N_M1ODT_1	G22	BiDi
BiDi	N16	IO_L72N_1	IO_L40P_GCLK11_M1A5_1	K20	BiDi
BiDi	P18	IO_L73P_1	IO_L40N_GCLK10_M1A6_1	K19	BiDi
BiDi	R19	IO_L73N_1	IO_L41P_GCLK9_IRDY1_M1RASN_1	H21	BiDi
BiDi	T19	IO_L74P_AWAKE_1	IO_L41N_GCLK8_M1CASN_1	H22	BiDi
BiDi	T20	IO_L74N_DOUT_BUSY_1	IO_L42P_GCLK7_M1UDM_1	M20	BiDi
Pwr In	W21	VCCO_1			
Pwr In	U18	VCCO_1			
Pwr In	R21	VCCO_1			
Pwr In	N18	VCCO_1			
Pwr In	L21	VCCO_1			
Pwr In	L16	VCCO_1			
Pwr In	J18	VCCO_1			
Pwr In	G21	VCCO_1			
Pwr In	F19	VCCO_1			
Pwr In	C21	VCCO_1			

FPGA > Xilinx > xc6slx45-2fgg484c

# XC6SLX45-2FGG484C (C)

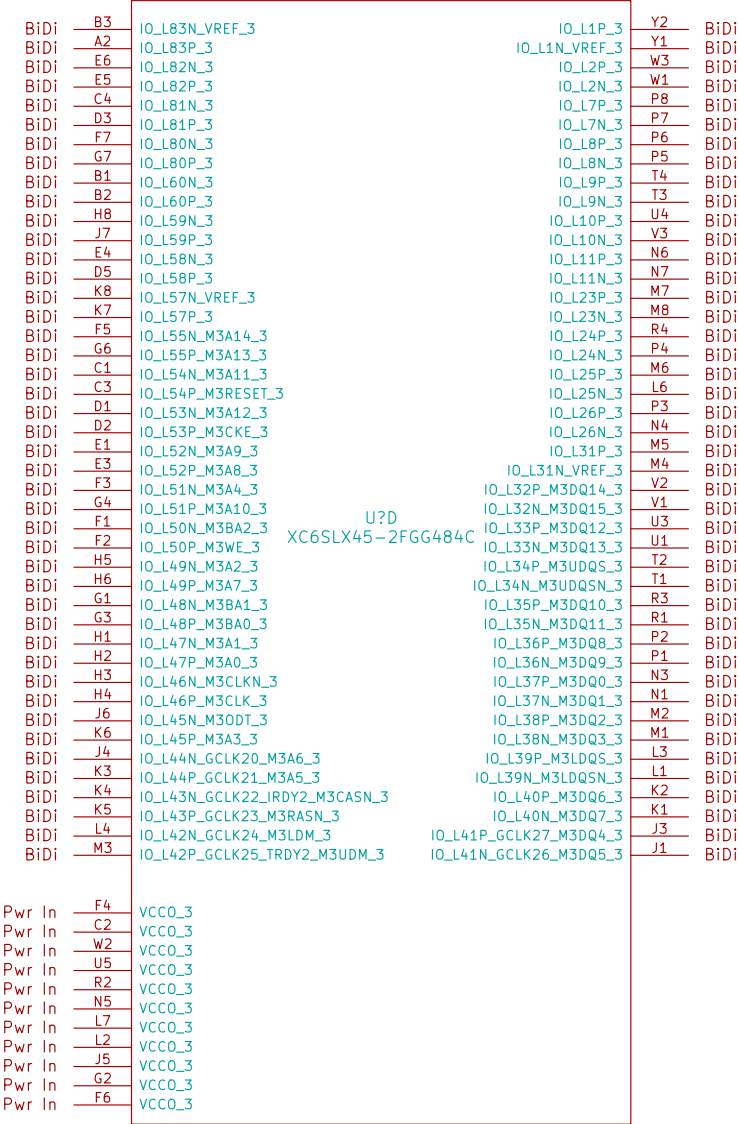
./xc6slx45-2fgg484c.lib

BiDi	<u>Y21</u>	IO_L1P_CCLK_2	IO_L65N_CS0_B_2	<u>T5</u>	BiDi
BiDi	<u>AA22</u>	IO_L1N_M0_CMPMISO_2	IO_L65P_INIT_B_2	<u>T6</u>	BiDi
BiDi	<u>AA21</u>	IO_L2P_CMPCLK_2	IO_L64N_D9_2	<u>AB2</u>	BiDi
BiDi	<u>AB21</u>	IO_L2N_CMPMOSI_2	IO_L64P_D8_2	<u>AA2</u>	BiDi
BiDi	<u>AA20</u>	IO_L3P_D0_DIN_MISO_MISO1_2	IO_L63N_2	<u>Y5</u>	BiDi
BiDi	<u>AB20</u>	IO_L3N_MOSI_CSI_B_MISO0_2	IO_L63P_2	<u>U6</u>	BiDi
BiDi	<u>T18</u>	IO_L4P_2	IO_L62N_D6_2	<u>Y4</u>	BiDi
BiDi	<u>T17</u>	IO_L4N_VREF_2	IO_L62P_D5_2	<u>W4</u>	BiDi
BiDi	<u>Y19</u>	IO_L5P_2	IO_L60N_2	<u>R7</u>	BiDi
BiDi	<u>AB19</u>	IO_L5N_2	IO_L60P_2	<u>T7</u>	BiDi
BiDi	<u>W18</u>	IO_L6P_2	IO_L59N_2	<u>R8</u>	BiDi
BiDi	<u>Y18</u>	IO_L6N_2	IO_L59P_2	<u>R9</u>	BiDi
BiDi	<u>T16</u>	IO_L7P_2	IO_L58N_2	<u>AB3</u>	BiDi
BiDi	<u>T15</u>	IO_L7N_2	IO_L58P_2	<u>Y3</u>	BiDi
BiDi	<u>U17</u>	IO_L8P_2	IO_L57N_2	<u>AB4</u>	BiDi
BiDi	<u>U16</u>	IO_L8N_2	IO_L57P_2	<u>AA4</u>	BiDi
BiDi	<u>V19</u>	IO_L9P_2	IO_L54N_2	<u>AB5</u>	BiDi
BiDi	<u>V18</u>	IO_L9N_2	IO_L54P_2	<u>Y5</u>	BiDi
BiDi	<u>R16</u>	IO_L10P_2	IO_L53N_2	<u>Y6</u>	BiDi
BiDi	<u>R15</u>	IO_L10N_2	IO_L53P_2	<u>W6</u>	BiDi
BiDi	<u>V17</u>	IO_L11P_2	IO_L52N_2	<u>U10</u>	BiDi
BiDi	<u>W17</u>	IO_L11N_2	IO_L52P_2	<u>T10</u>	BiDi
BiDi	<u>U14</u>	IO_L12P_D1_MISO2_2	IO_L51N_2	<u>U8</u>	BiDi
BiDi	<u>U13</u>	IO_L12N_D2_MISO3_2	IO_L51P_2	<u>T8</u>	BiDi
BiDi	<u>U15</u>	IO_L13P_M1_2	IO_L50N_2	<u>V9</u>	BiDi
BiDi	<u>V15</u>	IO_L13N_D10_2	IO_L50P_2	<u>U9</u>	BiDi
BiDi	<u>AA18</u>	IO_L14P_D11_2	IO_L49N_D4_2	<u>AB6</u>	BiDi
BiDi	<u>AB18</u>	IO_L14N_D12_2	IO_L49P_D3_2	<u>AA6</u>	BiDi
BiDi	<u>Y17</u>	IO_L15P_2	IO_L48N_RDWR_B_VREF_2	<u>AB7</u>	BiDi
BiDi	<u>AB17</u>	IO_L15N_2	IO_L48P_D7_2	<u>Y7</u>	BiDi
BiDi	<u>AA14</u>	IO_L16P_2	IO_L47N_2	<u>Y8</u>	BiDi
BiDi	<u>AB14</u>	IO_L16N_VREF_2	IO_L47P_2	<u>W9</u>	BiDi
BiDi	<u>Y16</u>	IO_L17P_2	IO_L46N_2	<u>V7</u>	BiDi
BiDi	<u>W15</u>	IO_L17N_2	IO_L46P_2	<u>W8</u>	BiDi
BiDi	<u>V13</u>	IO_L18P_2	IO_L45N_2	<u>AB8</u>	BiDi
BiDi	<u>W13</u>	IO_L18N_2	IO_L45P_2	<u>AA8</u>	BiDi
BiDi	<u>AA16</u>	IO_L19P_2	IO_L44N_2	<u>Y10</u>	BiDi
BiDi	<u>AB16</u>	IO_L19N_2	IO_L44P_2	<u>W10</u>	BiDi
BiDi	<u>W14</u>	IO_L20P_2	IO_L43N_2	<u>AB9</u>	BiDi
BiDi	<u>Y14</u>	IO_L20N_2	IO_L43P_2	<u>Y9</u>	BiDi
BiDi	<u>Y15</u>	IO_L21P_2	IO_L42N_2	<u>W11</u>	BiDi
BiDi	<u>AB15</u>	IO_L21N_2	IO_L42P_2	<u>V11</u>	BiDi
BiDi	<u>T12</u>	IO_L22P_2	IO_L41N_VREF_2	<u>AB10</u>	BiDi
BiDi	<u>U12</u>	IO_L22N_2	IO_L41P_2	<u>AA10</u>	BiDi
BiDi	<u>T14</u>	IO_L23P_2	IO_L40P_2	<u>R11</u>	BiDi
BiDi	<u>R13</u>	IO_L23N_2	IO_L40N_2	<u>T11</u>	BiDi
BiDi	<u>W12</u>	IO_L29P_GCLK3_2	IO_L32N_GCLK28_2	<u>AB11</u>	BiDi
BiDi	<u>Y12</u>	IO_L29N_GCLK2_2	IO_L32P_GCLK29_2	<u>Y11</u>	BiDi
BiDi	<u>Y13</u>	IO_L30P_GCLK1_D13_2	IO_L31N_GCLK30_D15_2	<u>AB12</u>	BiDi
BiDi	<u>AB13</u>	IO_L30N_GCLK0_USERCCLK_2	IO_L31P_GCLK31_D14_2	<u>AA12</u>	BiDi
Pwr In	<u>AA11</u>	VCC0_2			
Pwr In	<u>AA15</u>	VCC0_2			
Pwr In	<u>AA19</u>	VCC0_2			
Pwr In	<u>AA3</u>	VCC0_2			
Pwr In	<u>AA7</u>	VCC0_2			
Pwr In	<u>T13</u>	VCC0_2			
Pwr In	<u>T9</u>	VCC0_2			
Pwr In	<u>V12</u>	VCC0_2			
Pwr In	<u>V16</u>	VCC0_2			
Pwr In	<u>V8</u>	VCC0_2			
Pwr In	<u>W5</u>	VCC0_2			

FPGA > Xilinx > xc6slx45-2fgg484c

# XC6SLX45-2FGG484C (D)

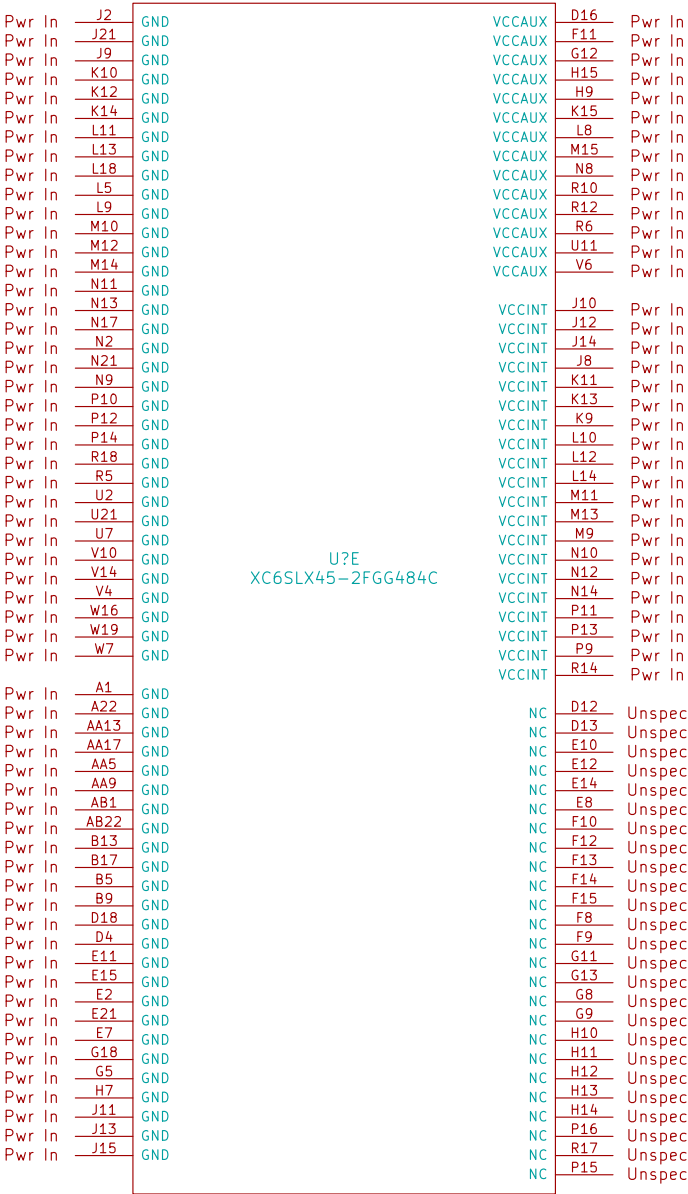
./xc6slx45-2fgg484c.lib



FPGA > Xilinx > xc6slx45-2fgg484c

# XC6SLX45-2FGG484C (E)

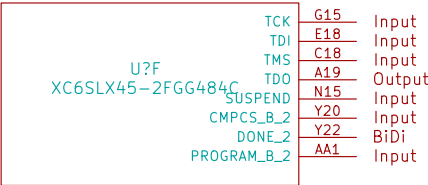
./xc6slx45-2fgg484c.lib



FPGA > Xilinx > xc6slx45-2fgg484c

# XC6SLX45-2FGG484C (F)

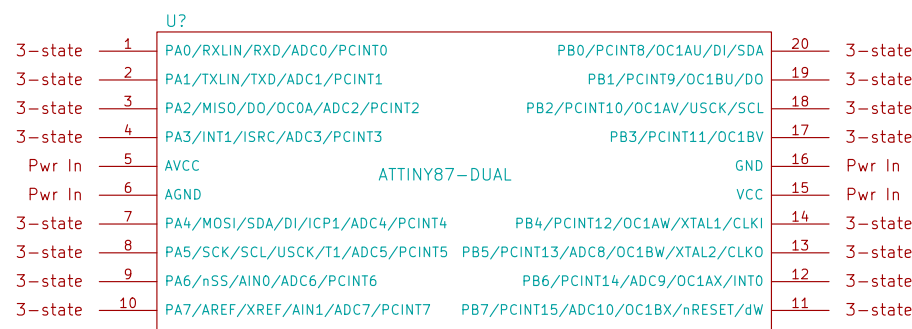
./xc6slx45-2fgg484c.lib





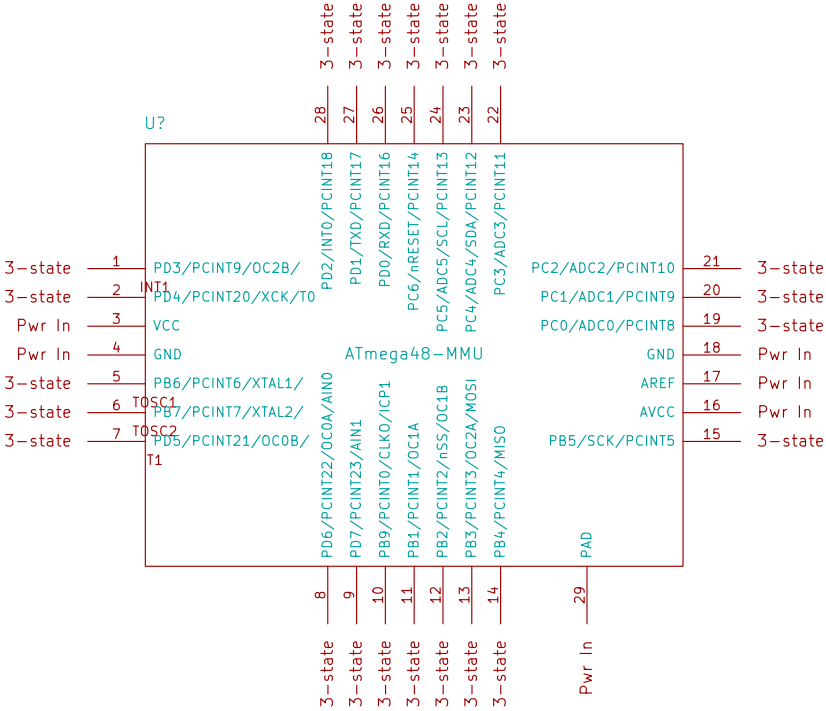
# ATTINY87-DUAL

./attiny87-dual.lib



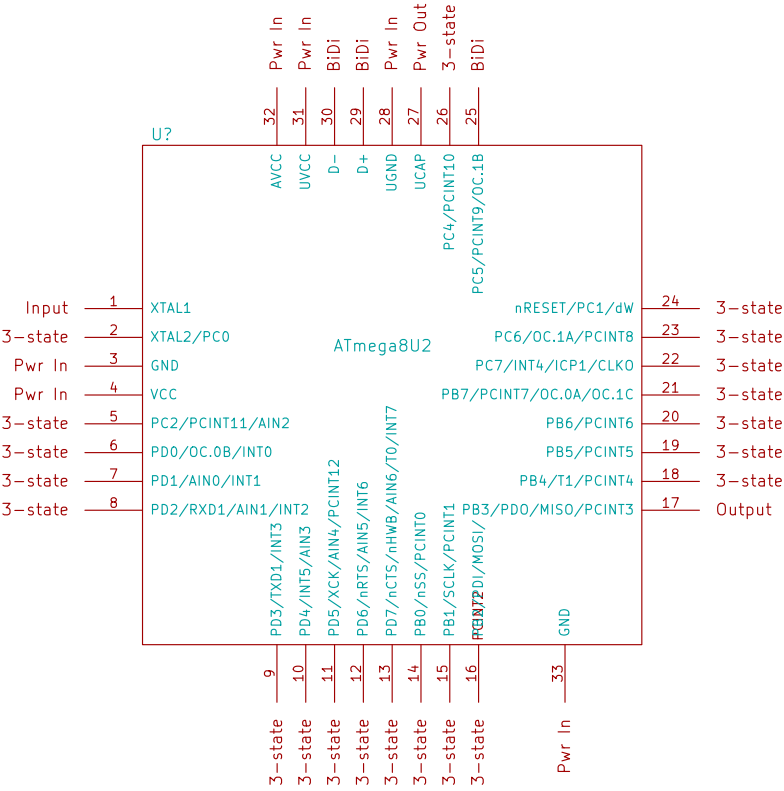
# ATmega48-MMU

./atmega48-mmu.lib



# ATmega8U2

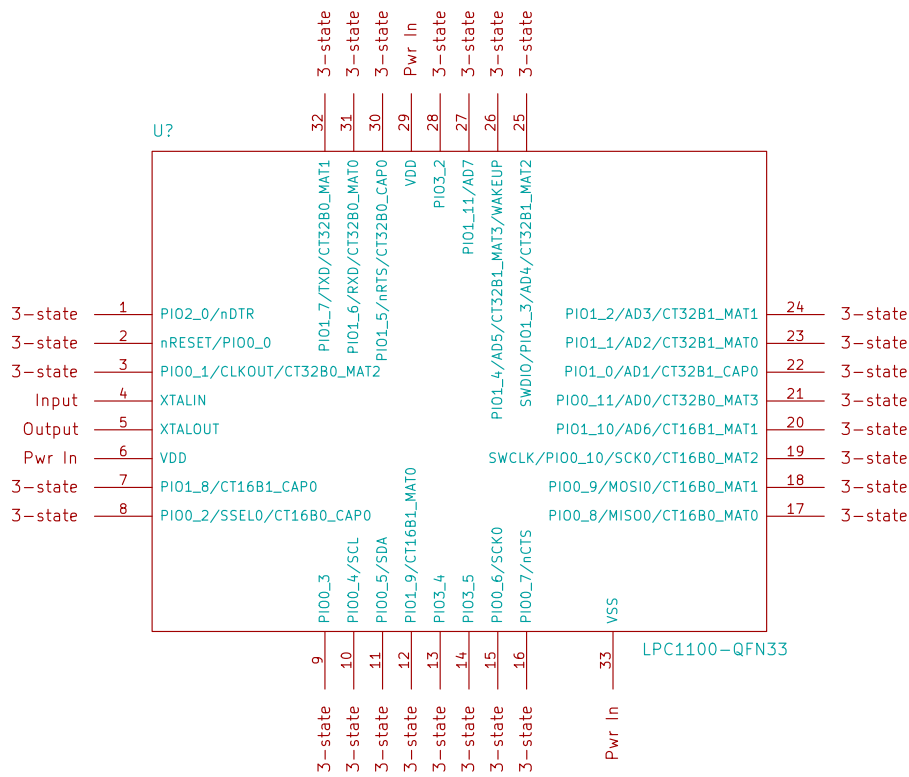
./atmega8u2.lib



MCU > LP1xxx > LPC1100-QFN33

# LPC1100-QFN33

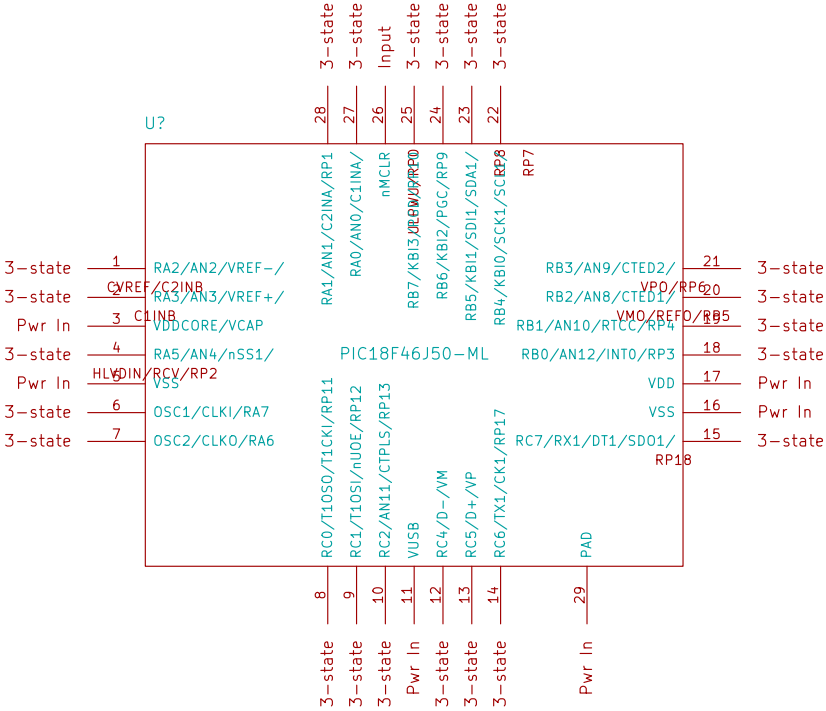
./lpc1100-qfn33.lib



MCU > PIC > pic18f46j50-ml

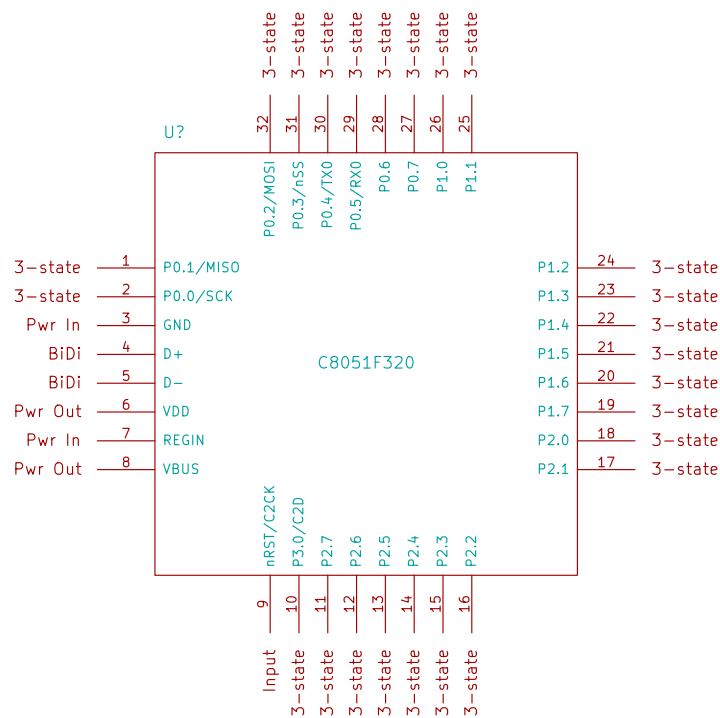
# PIC18F46J50-ML

./pic18f46j50-ml.lib



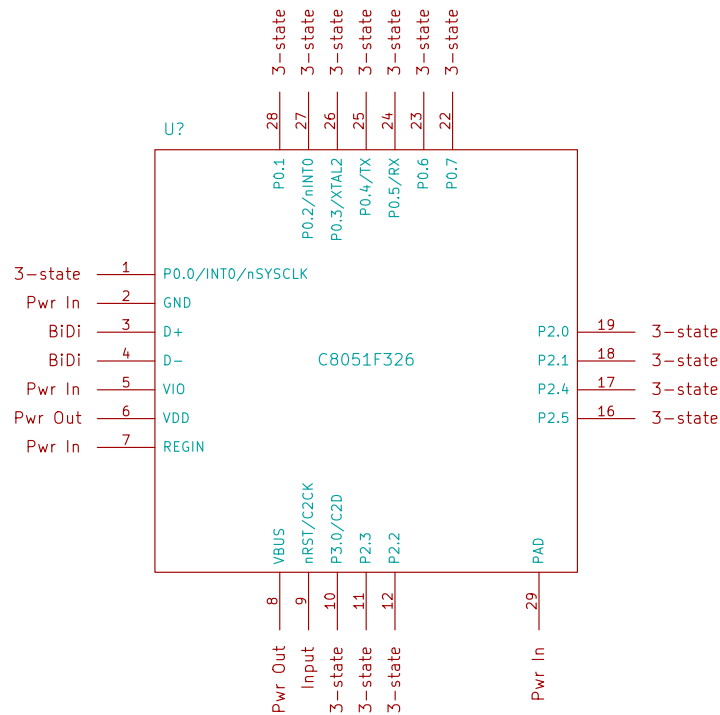
# C8051F320

./c8051f320.lib



# C8051F326

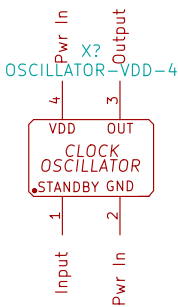
./c8051f326.lib



Oscillator > OSCILLATOR-VDD-4

# OSCILLATOR-VDD-4

./oscillator-cmos-out-4.lib



SO5032-050000-O3A-BBE-QA, OSC, 3.2X5MM, CER, 50.000MHZ, SMD  
OSCILLATOR

[http://downloads.qj-hardware.com/hardware/milkymist\\_one/datasheet/FPGA/Qi%20Hardware%20SO5032-0](http://downloads.qj-hardware.com/hardware/milkymist_one/datasheet/FPGA/Qi%20Hardware%20SO5032-0)



Power > powered

**POWERED**

./powered.lib



Power > Positive > 1V2

# 1V2

./pwr.lib

1V2  
Pwr In

Power > Positive > 1V8

# 1V8

./pwr.lib

1V8  
Pwr In

Power > Positive > 2V5

# 2V5

./pwr.lib

2V5  
Pwr In

Power > Positive > 3V3

# 3V3

./pwr.lib

3V3  
Pwr In

Power > Positive > 4V3

4V3

./pwr.lib

4V3  
Pwr In

Power > Positive > 5V

# 5V

./pwr.lib

5V  
-  
Pwr In

Power > Ground > GND

GND

./pwr.lib

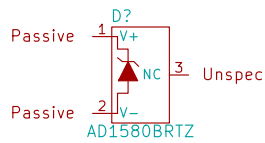
Pwr In ▾



Vendor > Analog > AD1580BRTZ

# AD1580BRTZ

./analog\_devices.lib



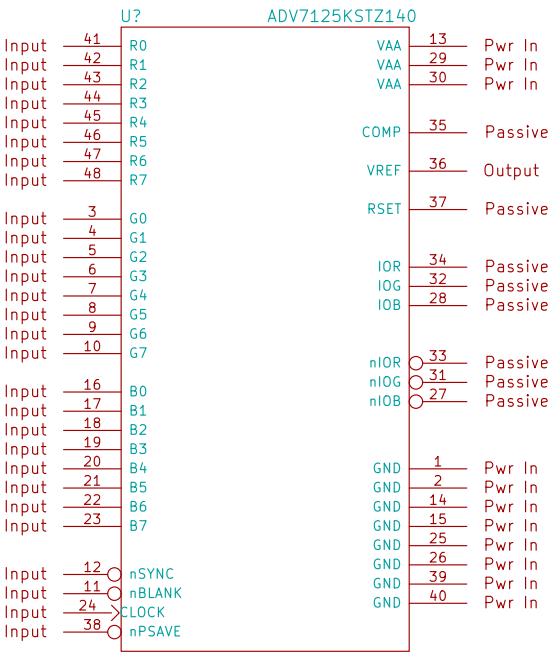
AD1580BRTZ, 1.2 V Micropower, Precision Shunt Voltage Reference, SOT-23-3  
Voltage Reference

[http://www.analog.com/static/imported-files/data\\_sheets/AD1580.pdf](http://www.analog.com/static/imported-files/data_sheets/AD1580.pdf)

Vendor > Analog > ADV7125KSTZ140

# ADV7125KSTZ140

./analog\_devices.lib



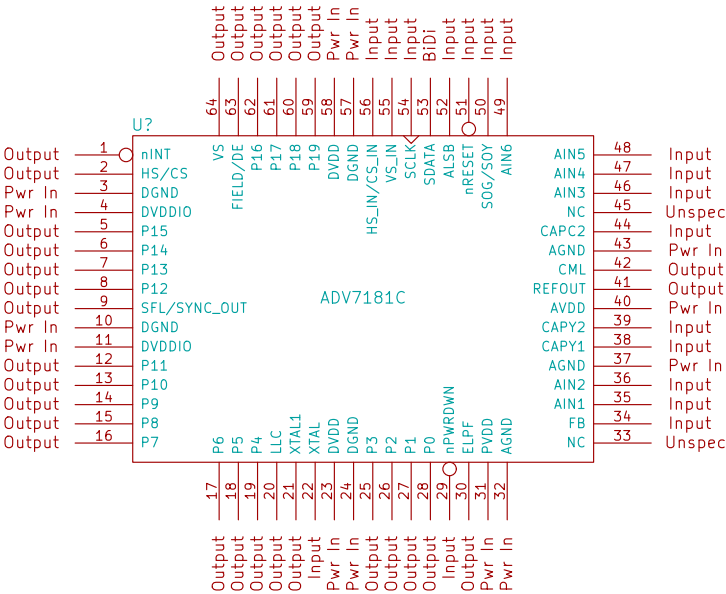
ADV7125KSTZ140, CMOS, 330 MHz, Triple 8-Bit High Speed Video DAC, LQFP-48  
Video DAC

[http://www.analog.com/static/imported-files/data\\_sheets/ADV7125.pdf](http://www.analog.com/static/imported-files/data_sheets/ADV7125.pdf)

Vendor > Analog > ADV7181C

# ADV7181C

./analog\_devices.lib



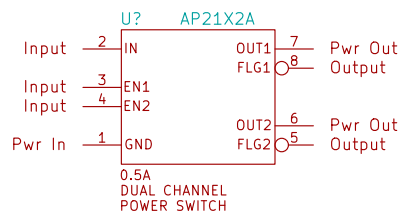
ADV7181C, IC VIDEO DECODER SDTV RGB 64LQFP  
VIDEO DECODER

[http://www.analog.com/static/imported-files/data\\_sheets/ADV7181C.pdf](http://www.analog.com/static/imported-files/data_sheets/ADV7181C.pdf)

Vendor > Diodes > AP21X2A

# AP21X2A

./diodes\_inc.lib



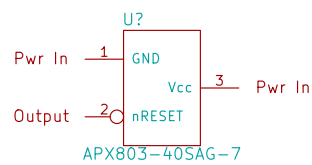
AP21X2A, IC PWR SW USB 2CH 0.5A 8-SOIC  $R_{ds(On)}=110\text{ m}\Omega$  Current Limit=700mA  
POWER SWITCH

[http://www.diodes.com/datasheets/AP2142A\\_52A.pdf](http://www.diodes.com/datasheets/AP2142A_52A.pdf)

Vendor > Diodes > APX803-40SAG-7

# APX803-40SAG-7

./diodes\_inc.lib



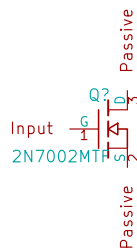
APX803-40SAG-7, IC MPU RESET CIRC 4.00V SOT23-3  
RESET IC

<http://www.diodes.com/datasheets/APX803.pdf>

Vendor > Fairchild > 2N7002MTF

# 2N7002MTF

./fairchild.lib



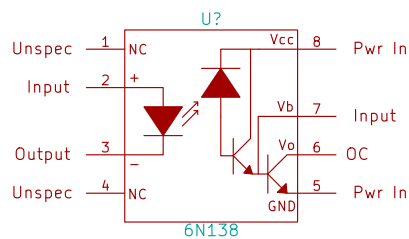
2N7002MTF, N-Channel Small Signal MOSFET  $R_{DS(on)} = 5.0 \, \Omega$   $BVDSS = 60 \, V$   $I_D = 200mA$ , SOT-23 N MOSFET

<http://www.fairchildsemi.com/ds/2N%2F2N7002MTF.pdf>

Vendor > Fairchild > 6N138

# 6N138

./fairchild.lib



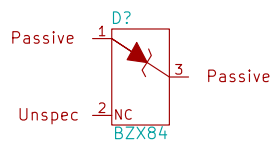
6N138S, 8-Pin SMD Single-Channel Low Input Current High Gain Split Darlington Output Optocoupler  
Darlington

<http://www.fairchildsemi.com/ds/6N/6N138.pdf>

Vendor > IR > BZX84

# BZX84

./nxp.lib



BZX84-C4V3, Voltage Regulator Diode, 4.3V, SOT-233, 250mW  
Voltage Regulator

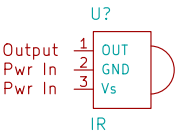
[http://www.nxp.com/acrobat\\_download/datasheets/BZX84\\_SERIES.pdf](http://www.nxp.com/acrobat_download/datasheets/BZX84_SERIES.pdf)



Vendor > IR > IR

# IR

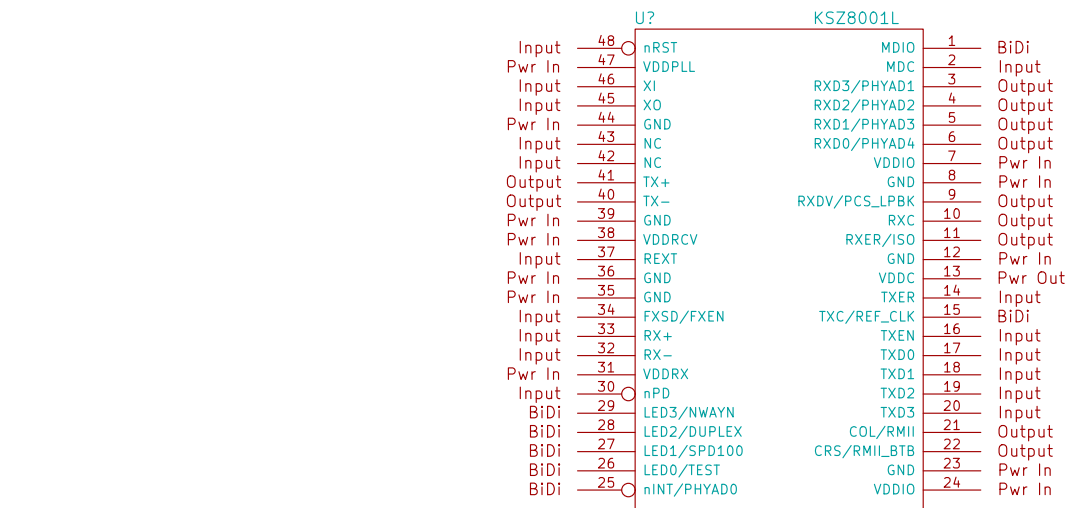
./ir.lib



Vendor > Micrel > KSZ8001L

# KSZ8001L

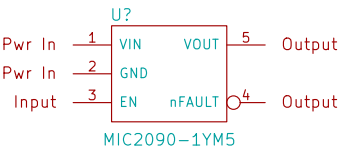
./micrel.lib



Vendor > Micrel > MIC2090-1YM5

# MIC2090-1YM5

./micrel.lib



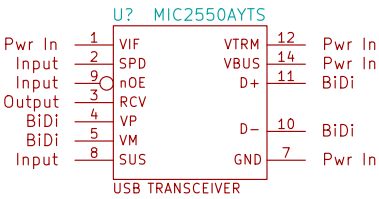
MIC2090-1YM5 TR, PMIC - Power Distribution Switches High Side Switch 50MA Rds (On)=1.2 Ohm Current  
PMIC

[http://www.micrel.com/\\_PDF/mic2090\\_1.pdf](http://www.micrel.com/_PDF/mic2090_1.pdf)

Vendor > Micrel > MIC2550AYTS

# MIC2550AYTS

./micrel.lib

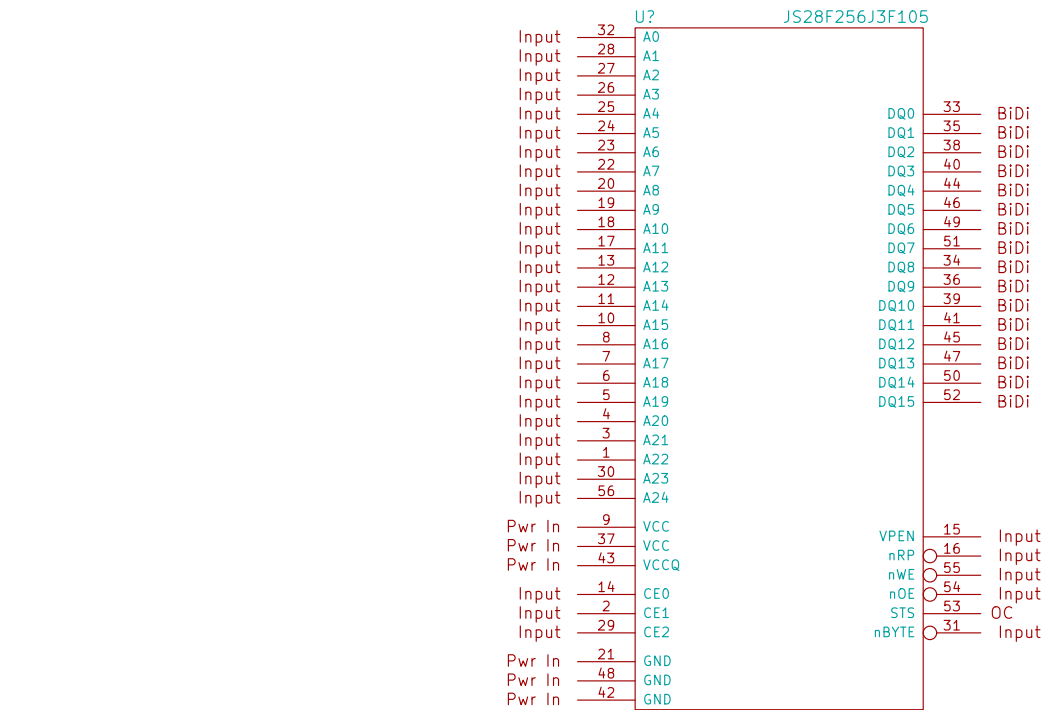


MIC2550AYTS, Universal Serial Bus Transceiver, 14-TSSOP  
USB TRANSCEIVER  
[http://www.micrel.com/\\_PDF/mic2550a.pdf](http://www.micrel.com/_PDF/mic2550a.pdf)

Vendor > Micron > JS28F256J3F105

# JS28F256J3F105

./js28f256j3f105.lib



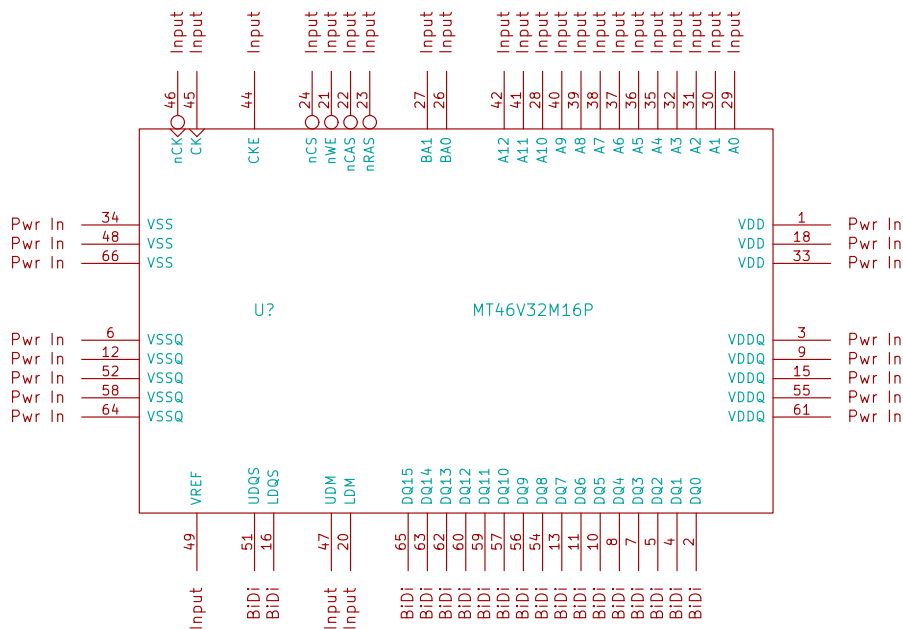
JS28F256J3F105, FLASH 256Mbit, 105ns, 3V, 56-Lead TSOP  
FLASH

[http://www.micron.com/parts/nor-flash/parallel-nor-flash/~media/Documents/Products/Data%20Sheet/NOR%](http://www.micron.com/parts/nor-flash/parallel-nor-flash/~media/Documents/Products/Data%20Sheet/NOR%20Flash/JS28F256J3F105.pdf)

Vendor > Micron > MT46V32M16P

# MT46V32M16P

./micron.lib



MT46V32M16P, 8 Meg x 16 x 4 banks, VDD = +2.6V ´0.1V, VDDQ = +2.6V ´0.1V (DDR400), 5ns, DDR400  
DDR400  
<http://download.micron.com/pdf/datasheets/dram/ddr/512MBDDR4x4x8x16.pdf>

Vendor > Microphone > MCE-100

# MCE-100

./microphone.lib

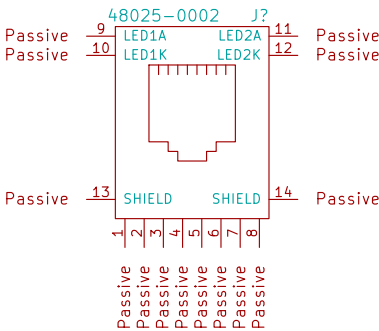


MCE-100, Electret microphone, DC:10V 5.6mV/Pa, 9.7 x 6.7mm (Diameter x Height)  
microphone  
<http://www.farnell.com/datasheets/359234.pdf>

Vendor > Molex > 48025-0002

# 48025-0002

./molex.lib



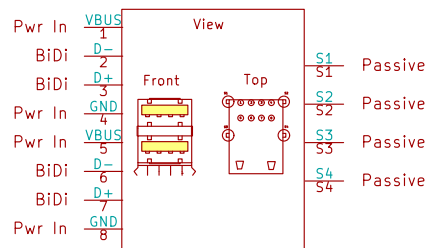
48025-0002, Telecom & Ethernet Connectors RA MOD JACK 8/8 W/ LEDS  
ETHERNET CON  
[http://www.molex.com/pdm\\_docs/sd/480250002\\_sd.pdf](http://www.molex.com/pdm_docs/sd/480250002_sd.pdf)



Vendor > Molex > 67298-4090

67298-4090

./molex.lib



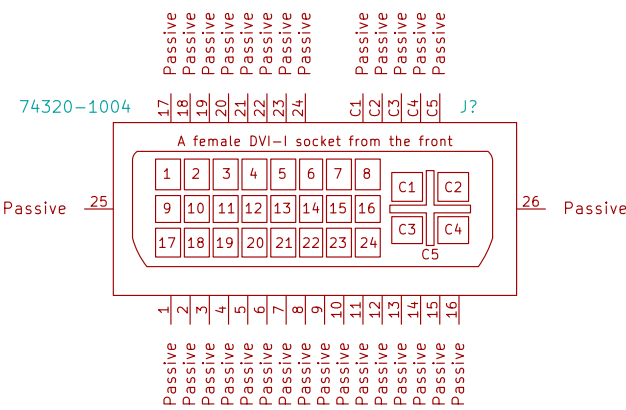
67298-4090, CONN RECEPT USB STACKED R/A WHT HORIZONTAL THROUGH HOLE  
USB CONN

[http://www.molex.com/pdm\\_docs/sd/672984090\\_sd.pdf](http://www.molex.com/pdm_docs/sd/672984090_sd.pdf)

Vendor > Molex > 74320-1004

# 74320-1004

./molex.lib



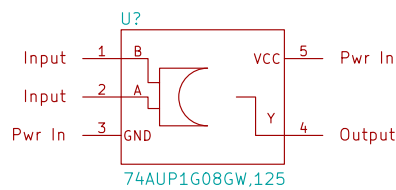
74320-1004, CONN RECEPT ANALOG DIGITAL DVI  
DVI CON

[http://www.molex.com/pdm\\_docs/sd/743201004\\_sd.pdf](http://www.molex.com/pdm_docs/sd/743201004_sd.pdf)

Vendor > NXP > 74AUP1G08GW,125

# 74AUP1G08GW,125

./nxp.lib



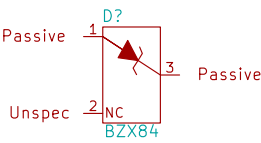
74AUP1G08GW,125; IC 2-IN AND GATE LP 5-TSSOP  
AND

[http://www.nxp.com/documents/data\\_sheet/74AUP1G08.pdf](http://www.nxp.com/documents/data_sheet/74AUP1G08.pdf)

Vendor > NXP > BZX84

# BZX84

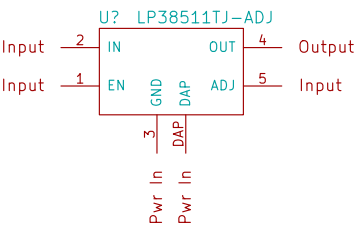
./nxp.lib



Vendor > TI > LP38511TJ-ADJ

# LP38511TJ-ADJ

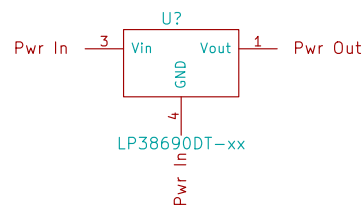
./ti.lib



Vendor > TI > LP38690DT-xx

# LP38690DT-xx

./ti.lib

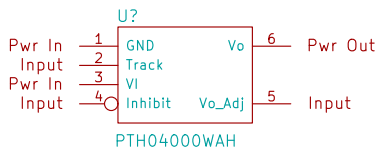


LP38690DT, 1A Low Dropout CMOS Linear Regulators Stable with Ceramic Output Capacitors, TO-252 Linear Regulator  
<http://www.ti.com/lit/ds/symlink/lp38690.pdf>

Vendor > TI > PTH04000WAH

# PTH04000WAH

./ti.lib

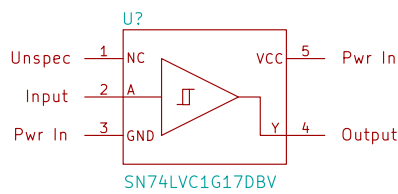


PTH04000WAH, 3-A, 3.3/5-V Input Adjustable Switching Regulator with Auto-Track Sequencing  
Switching Regulator  
<http://www.ti.com/lit/ds/symlink/pth04000w.pdf>

Vendor > TI > SN74LVC1G17DBV

# SN74LVC1G17DBV

./ti.lib



SN74LVC1G17DBVR, SINGLE SCHMITT-TRIGGER BUFFER, SOT-235  
SCHMITT-TRIGGER BUFFER

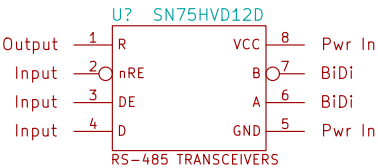
<http://www.ti.com/lit/ds/symlink/sn74lvc1g17.pdf>



Vendor > TI > SN75HVD12D

# SN75HVD12D

./ti.lib

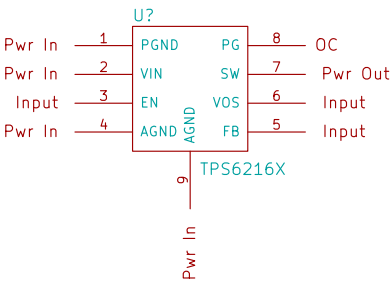


SN75HVD12D, 3.3V RS-485 TRANSCEIVERS, 1Mbps, SOIC-8  
RS-485 TRANSCEIVERS  
<http://www.ti.com/lit/ds/symlink/sn75hvd12.pdf>

Vendor > TI > TPS6216x

# TPS6216X

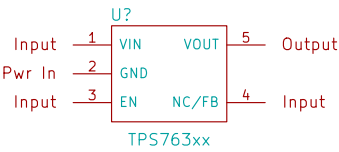
./tps6216x.lib



Vendor > TI > TPS763xx

# TPS763xx

./ti.lib



TPS763xx, LOW POWER 150mA LOW DROPOUT LINEAR REGULATORS SOT-23, VARIABLE  
LINEAR REGULATOR  
<http://www.ti.com/lit/ds/symlink/tps76301.pdf>

Vendor > Varistor > EZJ-Z0V80010

# EZJ-Z0V80010

./varistor.lib



EZJ-Z0V80010, Panasonic - ECG, VARISTOR MULTILAYER 80V 0402 Maximum DC Volts=10VDC Maximum  
VARISTOR

<http://industrial.panasonic.com/www-data/pdf/AWC0000/AWC0000CE2.pdf>

Unclassified > werner-17042012 > DIODE-SOT-AXC

# DIODE-SOT-AXC

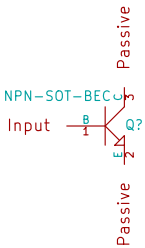
./device\_sot.lib



Unclassified > werner-17042012 > NPN-SOT-BEC

# NPN-SOT-BEC

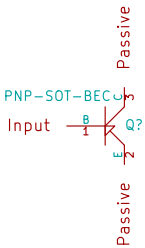
./device\_sot.lib



Unclassified > werner-17042012 > PNP-SOT-BEC

# PNP-SOT-BEC

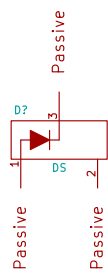
./device\_sot.lib



Unclassified > werner-17042012 > DS

# DS

./dual\_diode.lib

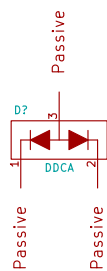


Single diode in SOT package



# DDCA

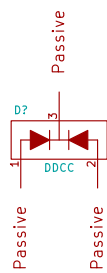
./dual\_diode.lib



Dual diode in SOT package, common anode

# DDCC

./dual\_diode.lib

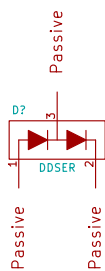


Dual diode in SOT package, common cathode

Unclassified > werner-17042012 > DDSER

# DDSER

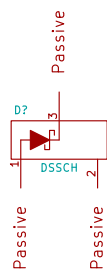
./dual\_diode.lib



Dual diode in SOT package, anode follows cathode

# DSSCH

./dual\_diode.lib

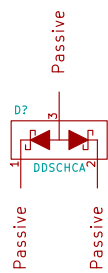


Single Schottky diode in SOT package

Unclassified > werner-17042012 > DDSCHCA

# DDSCHCA

./dual\_diode.lib

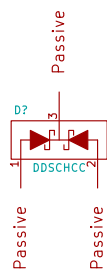


Dual Schottky diode in SOT package, common anode

Unclassified > werner-17042012 > DDSCHCC

# DDSCHCC

./dual\_diode.lib

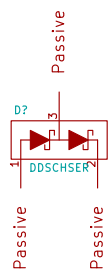


Dual Schottky diode in SOT package, common cathode

Unclassified > werner-17042012 > DDSCHSER

# DDSCHSER

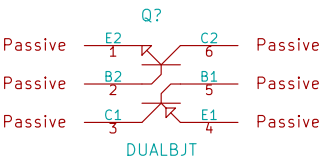
./dual\_diode.lib



Dual Schottky diode in SOT package, anode follows cathode

# DUALBJT

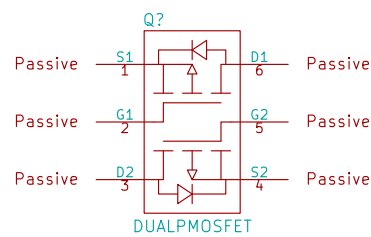
./dualbjt.lib





# DUALPMOSFET

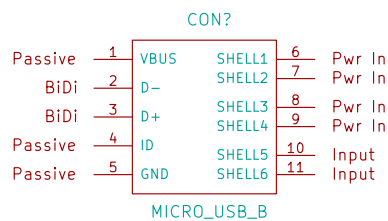
./dualpmosfet.lib



Unclassified > werner-17042012 > MICRO\_USB\_B

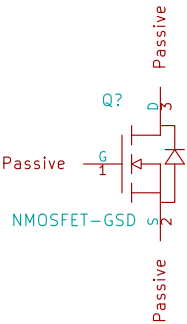
MICRO\_USB\_B, ZX62-B-5PA

./micro\_usb\_b.lib



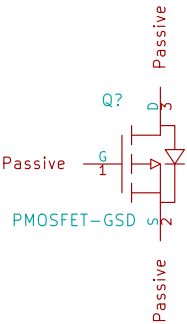
# NMOSFET-GSD

./nmosfet-gsd.lib



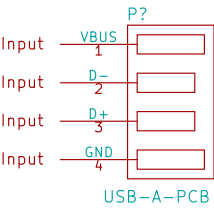
# PMOSFET-GSD

./pmosfet-gsd.lib



# USB-A-PCB

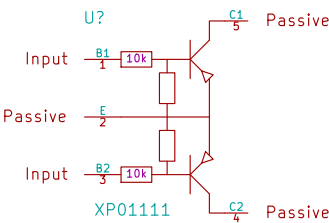
./usb-a-pcb.lib



Unclassified > werner-17042012 > XP01111

# XP01111

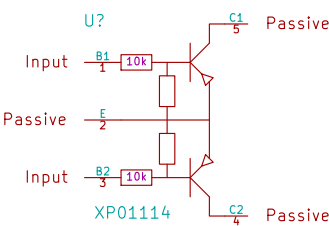
./dual\_trans.lib



Dual PNP transistor with 10k base and 10k EB resistor

XP01114

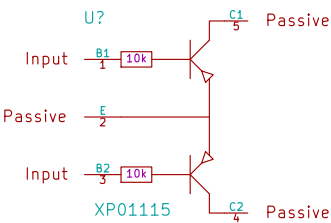
./dual\_trans.lib



Dual PNP transistor with 10k base and 47k EB resistor

# XP01115

./dual\_trans.lib

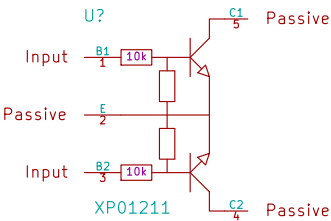


Dual PNP transistor with 10k base resistor



# XP01211

./dual\_trans.lib

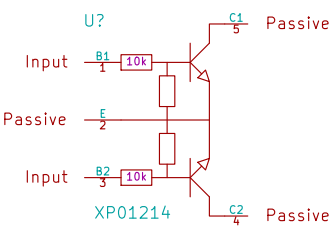


Dual NPN transistor with 10k base and 10k EB resistor

Unclassified > werner-17042012 > XP01214

# XP01214

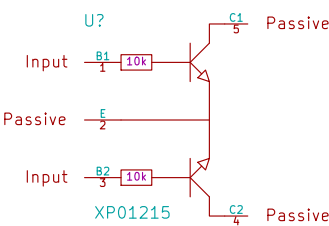
./dual\_trans.lib



Dual NPN transistor with 10k base and 47k EB resistor

# XP01215

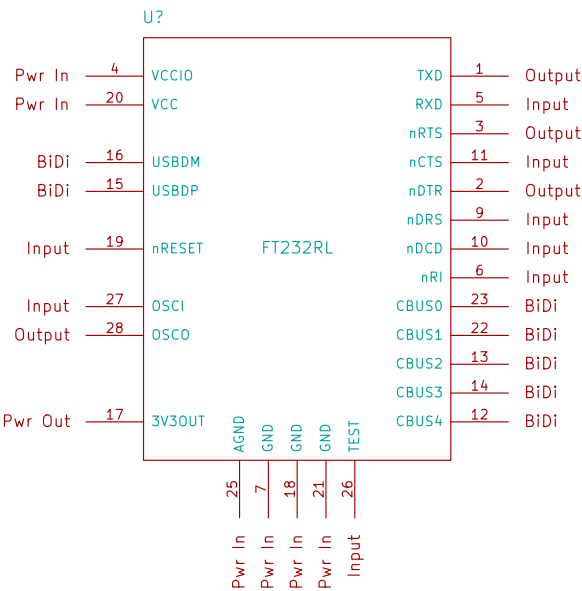
./dual\_trans.lib



Dual NPN transistor with 10k base resistor

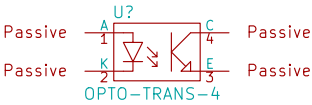
# FT232RL

./ft232r1.lib



# OPTO-TRANS-4

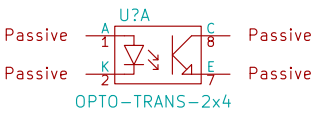
./opto-trans-4.lib



Unclassified > werner-17042012 > OPTO-TRANS-2x4

# OPTO-TRANS-2x4 (A)

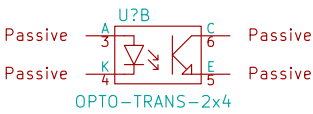
./opto-trans-8.lib



Unclassified > werner-17042012 > OPTO-TRANS-2x4

# OPTO-TRANS-2x4 (B)

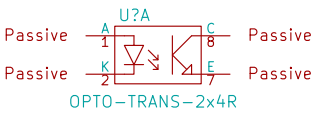
./opto-trans-8.lib



Unclassified > werner-17042012 > OPTO-TRANS-2x4R

# OPTO-TRANS-2x4R (A)

./opto-trans-8.lib

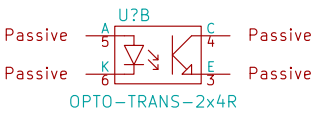




Unclassified > werner-17042012 > OPTO-TRANS-2x4R

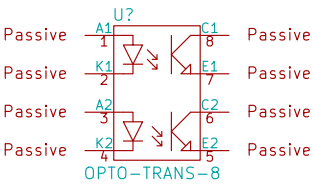
# OPTO-TRANS-2x4R (B)

./opto-trans-8.lib



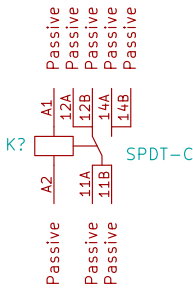
# OPTO-TRANS-8

./opto-trans-8.lib



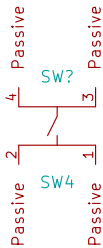
# SPDT-C

./spdt-c.lib



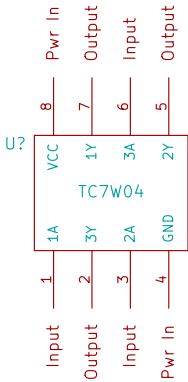
SW4

./sw4.lib



# TC7W04

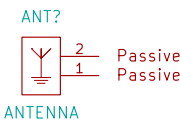
./74hc04-3.lib



Unclassified > werner-17042012 > ANTENNA

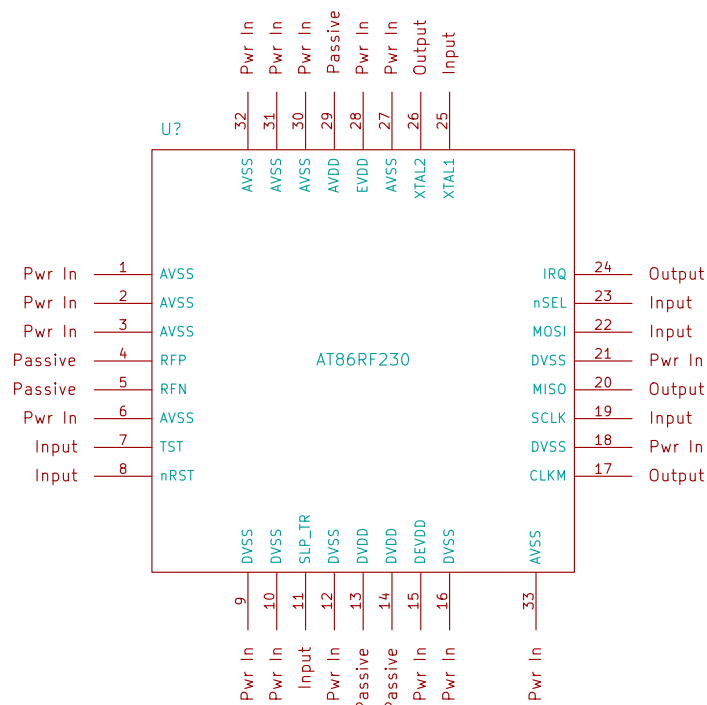
# ANTENNA

./antenna.lib



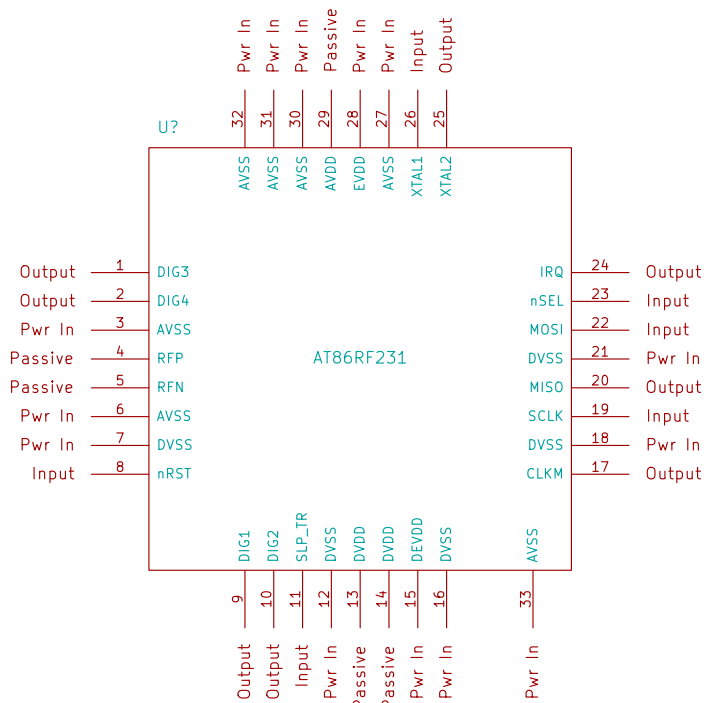
# AT86RF230

./at86rf230.lib



# AT86RF231

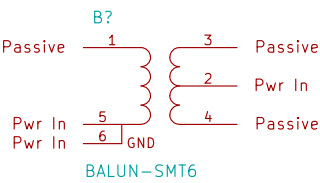
./at86rf231.lib





# BALUN-SMT6

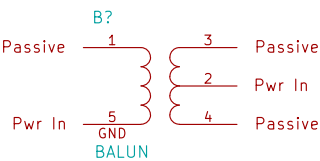
./balun-smt6.lib



Unclassified > werner-17042012 > BALUN

# BALUN

./balun.lib



# XTAL-4

./xtal-4.lib

