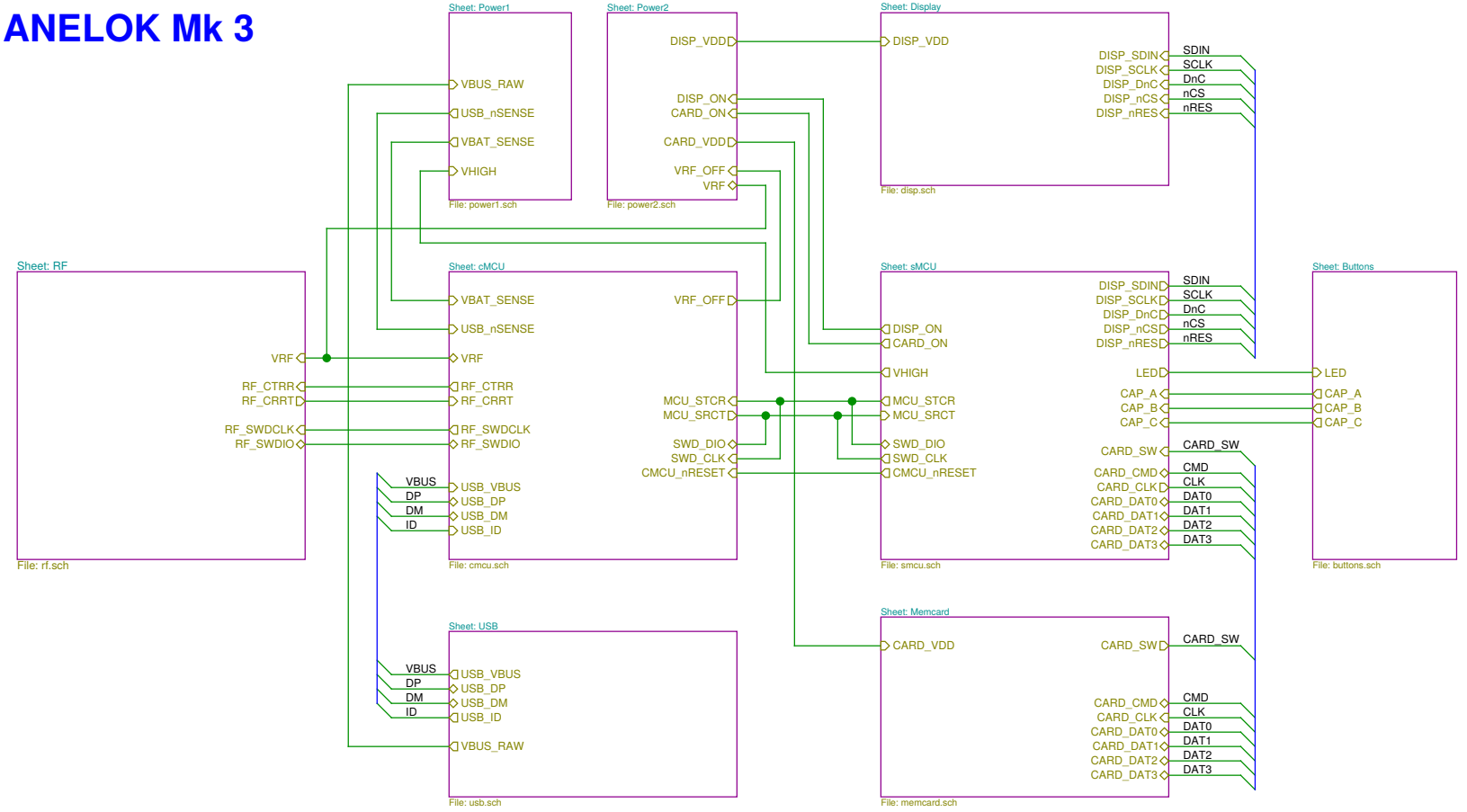
ANELOK Mk 3



Capacitor selection

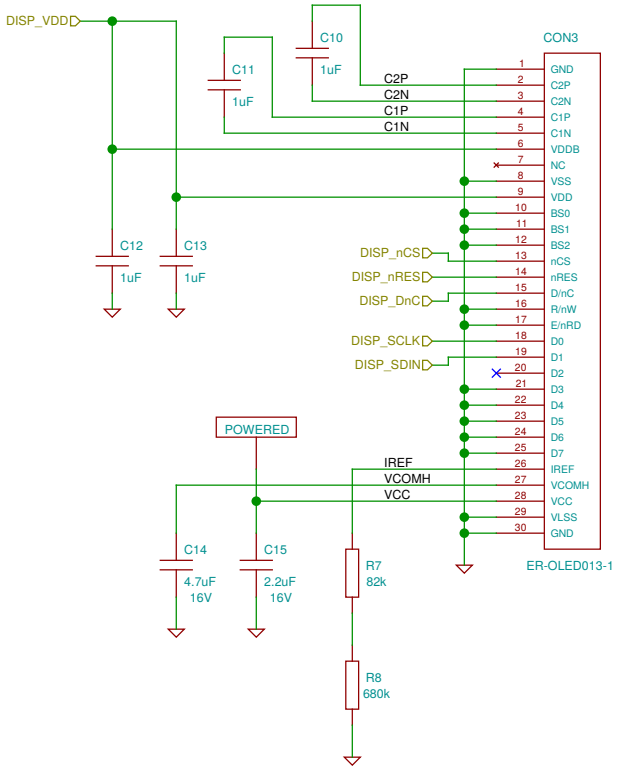
Unless indicated otherwise, all capacitors should be X5R or X6S, or better (X7R, NP0, etc.)

Y5V is cheaper but very sensitive to temperature, ages rapidly, etc.

If no voltage is specified, use >= 6.3 V

Werner Almesberger		
Sheet: / File: anelok.sch		
Title: Anelok Password Safe		
Size: A4	Date: 2016-02-05	Rev:
Plotted by eeshow 4163d56 20170304-02:28Z		Id: 1/10

Display



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File: disp.sch		
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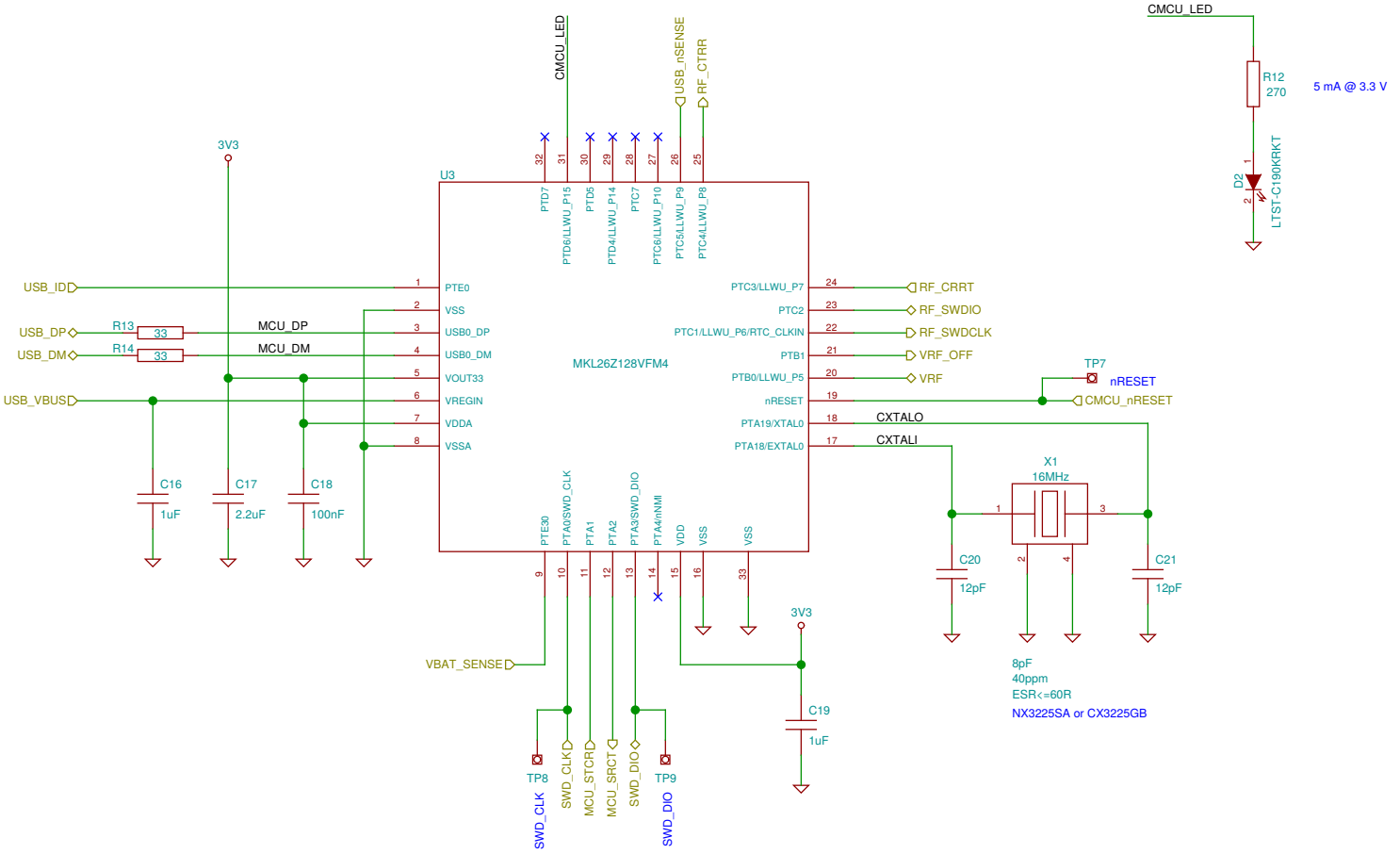
Size: A4	Date: 2016-02-05	Rev:
Plotted by eeshow 4163d56 20170304-02:28Z		Id: 2/10

Communications MCU



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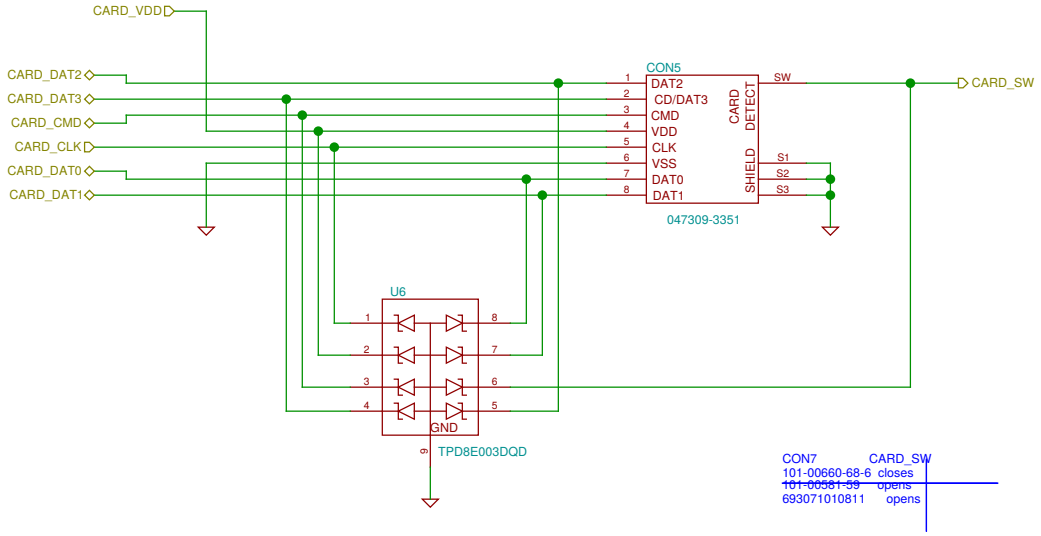
Size: A4	Date: 2016-02-05	Rev:
Plotted by eeshow 4163d56 20170304-02:28Z		Id: 3/10

Memory card



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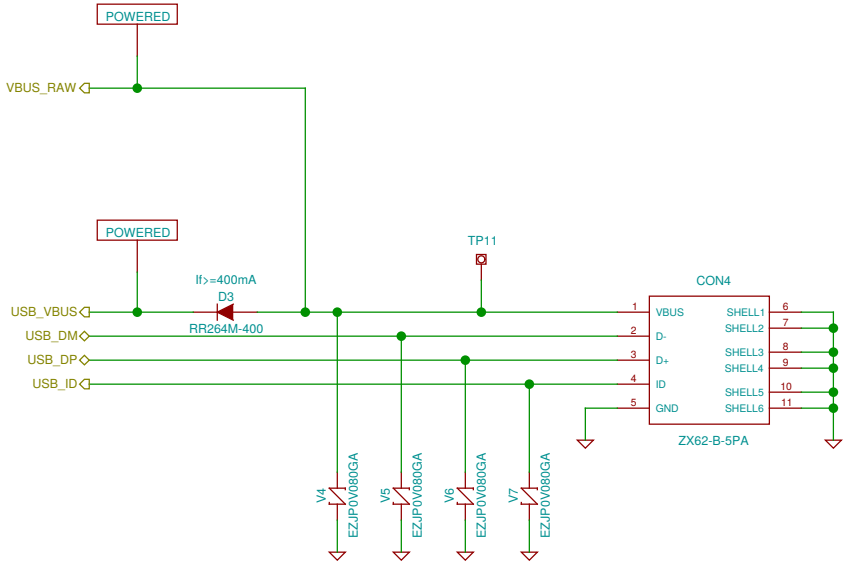
USB



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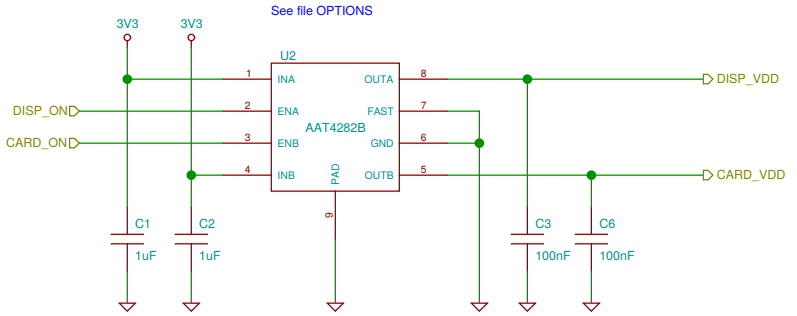
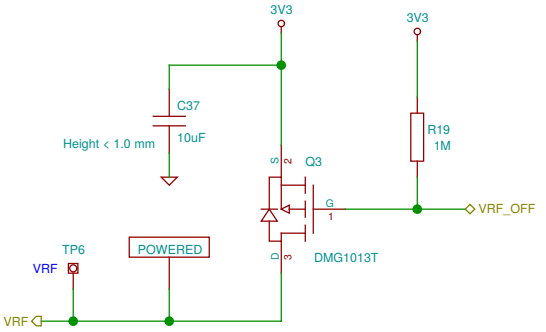
Power 2





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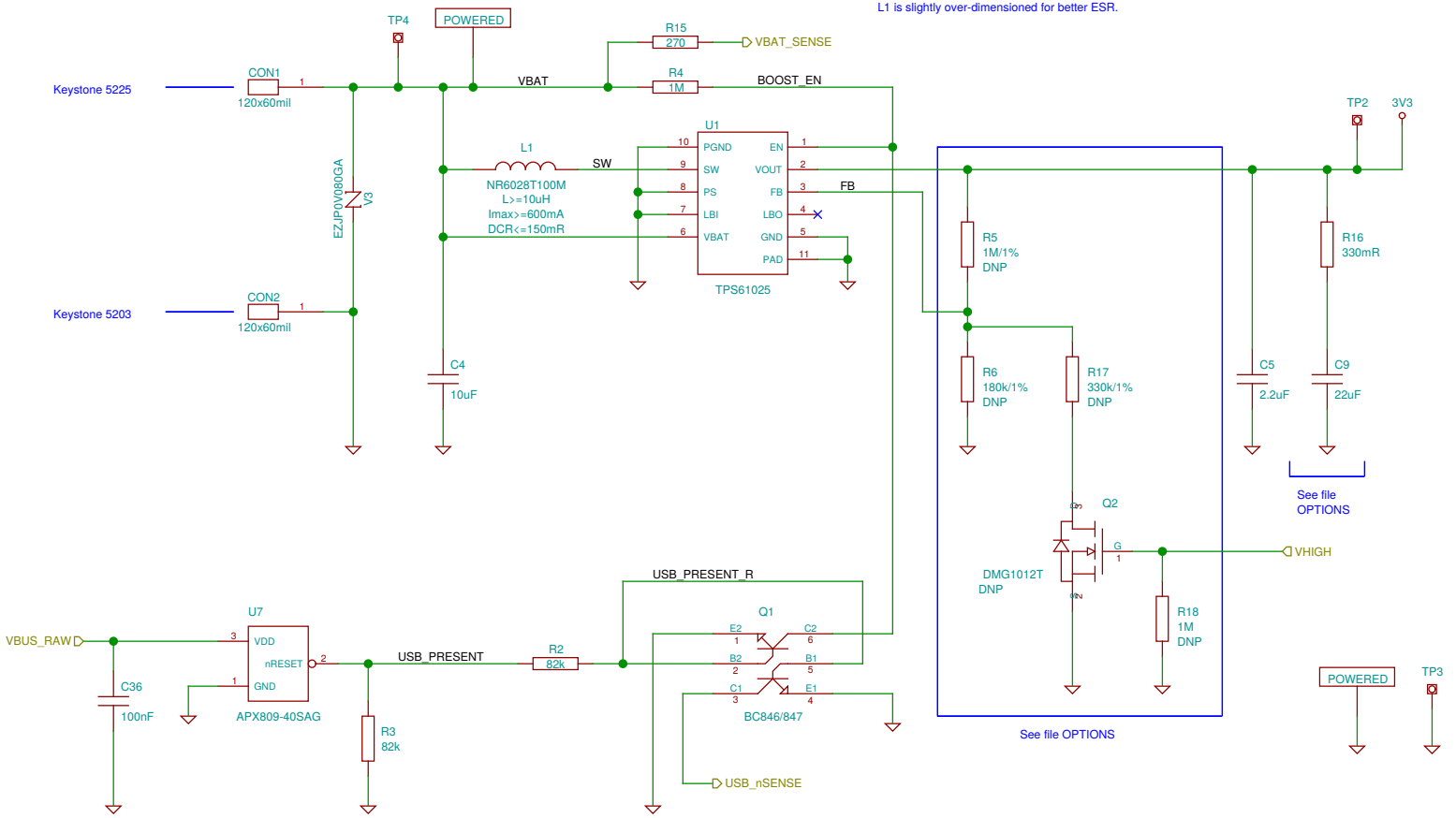
Size: A4	Date: 2016-02-05	Rev:
Plotted by eeshow 4163d56 20170304-02:28Z		Id: 6/10

Power 1

Design for continuous output current of 120 mA.
L1 is slightly over-dimensioned for better ESR.



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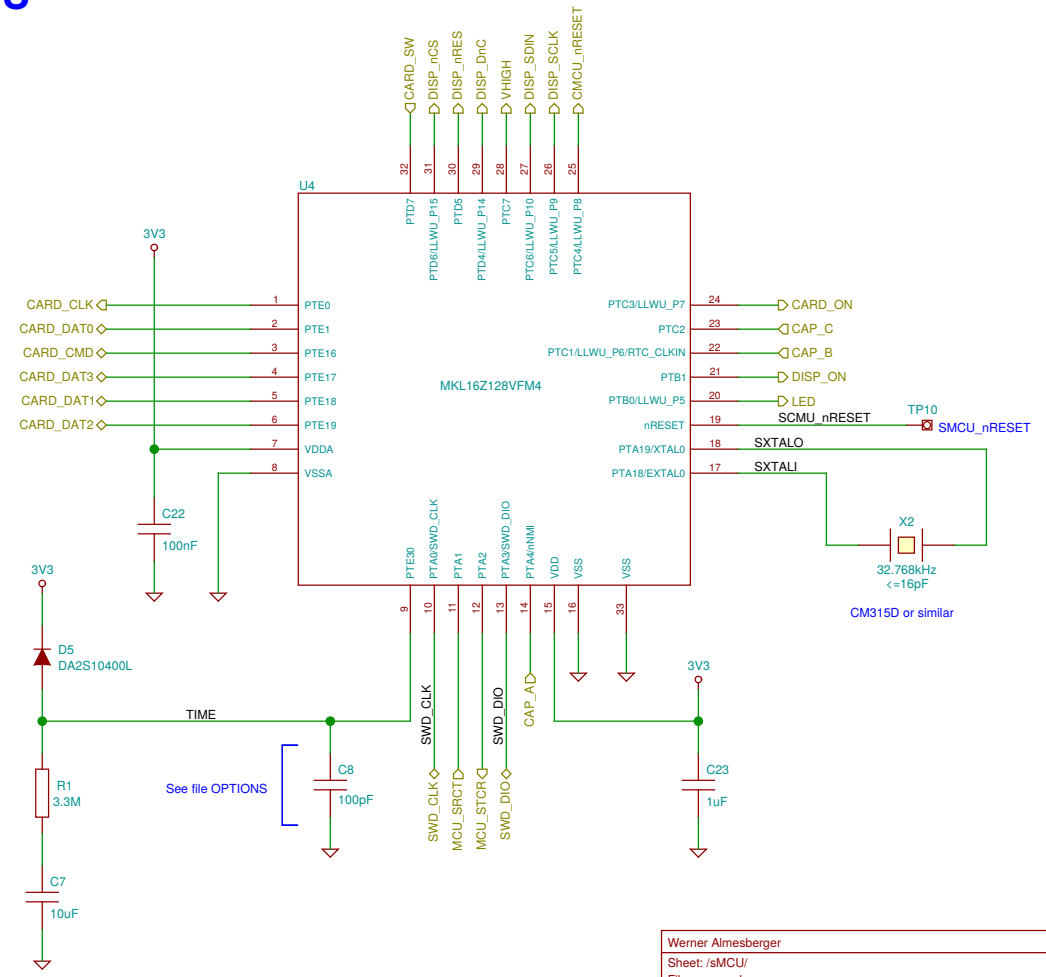
Plotted by eeshow 4163d56 20170304-02:28Z		Id: 7/10

Secure MCU



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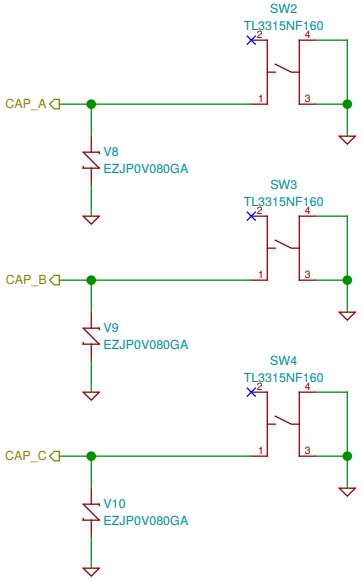
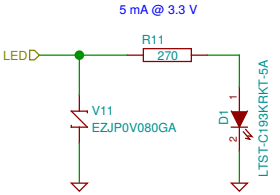
Plotted by eeshow 4163d56 20170304-02:28Z		Id: 8/10

Buttons





Werner Almesberger		
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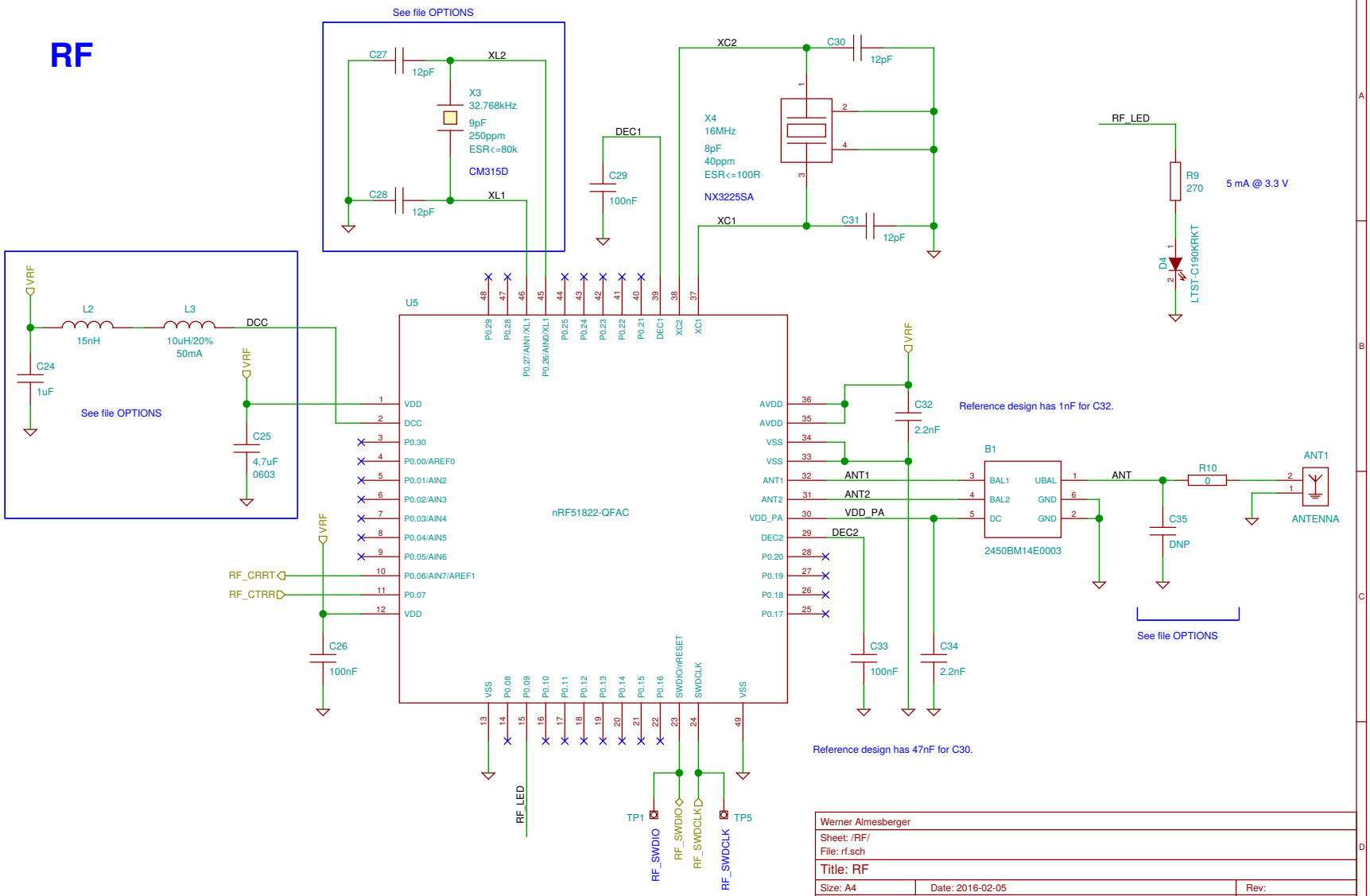
Size: A4	Date: 2016-02-05	Rev:
Plotted by eeshow 4163d56 20170304-02:28Z		Id: 9/10

RF



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Title: RF		
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Plotted by eeshow 4163d56 20170304-02:28Z		Id: 10/10